Mariana Block Diagram

2008.06.01
C Stage

VTERM(+0.9V)
VTT(+1.05V)
+1.5VSUS
+1.5V
+1.8VSUS
+1.8V
+2.5V
3VPCU
+3.3V
+3.3VSUS
LCD_3.3V
LCD_5V
+5V

XDP

Thermal Sensor

Diamondville
FCBGA8 437
VCORE:+1.196 ~ +0.748
VCCP:+1.05V
VCCA:+1.8V or +1.5V

VID[0:6]

CPU VCORE
IMVP 6

+/- CPU_CLK

Clock Gengerator
CK505M

+/- HCLK

FSB 533MHz

8.9" or 12" LCD

LVDS

CRT

RGB

945GMS/GSE
FCBGA 998

CHB

DDR2
400/533
SO-DIMM
RANK 2
(Up to 1GB)

CHA

DDR2 400/533
512MB DOWN
RANK 1
64M X 16 X 4

X2 DMI

RJ-45

10/100 Ethernet
BCM5906

PCI-e

HP/Mic Audio Jack

Int. Mic

HDA CODEC
ALC269

HD Audio

Int. SPK

Amplify

ICH7M
BGA 652

SATA

2.5" HDD

IDE

SSD 4/8/16GB
(Option)

PCI-e/USB

Mini PCIe Slot

WLAN / WWAN
Module

USB

Card Reader
RTL5158E

4 in 1 Socket
SD/MMC/MS/MS-Pro

USB

USB PORT X 2

34mm New Card

PCI-e

LPC BUS

USB

Camera Conn.

Camera Module
(Option)

ENE
KB3310

SPI Flash

USB

Bluetooth
(Option)

Int. KB

T/P

Battery

Charger

Quanta Computer Inc.
PROJECT : FL1

Size	Document Number	Rev
	Block Diagram	1A

Date: Saturday, June 07, 2008 Sheet 1 of 34

Table of Contents

PAGE	DESCRIPTION
1	Schematic Block Diagram
2	

Power States

POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
VIN	10V~+19V				
5VPCU					
3VPCU					

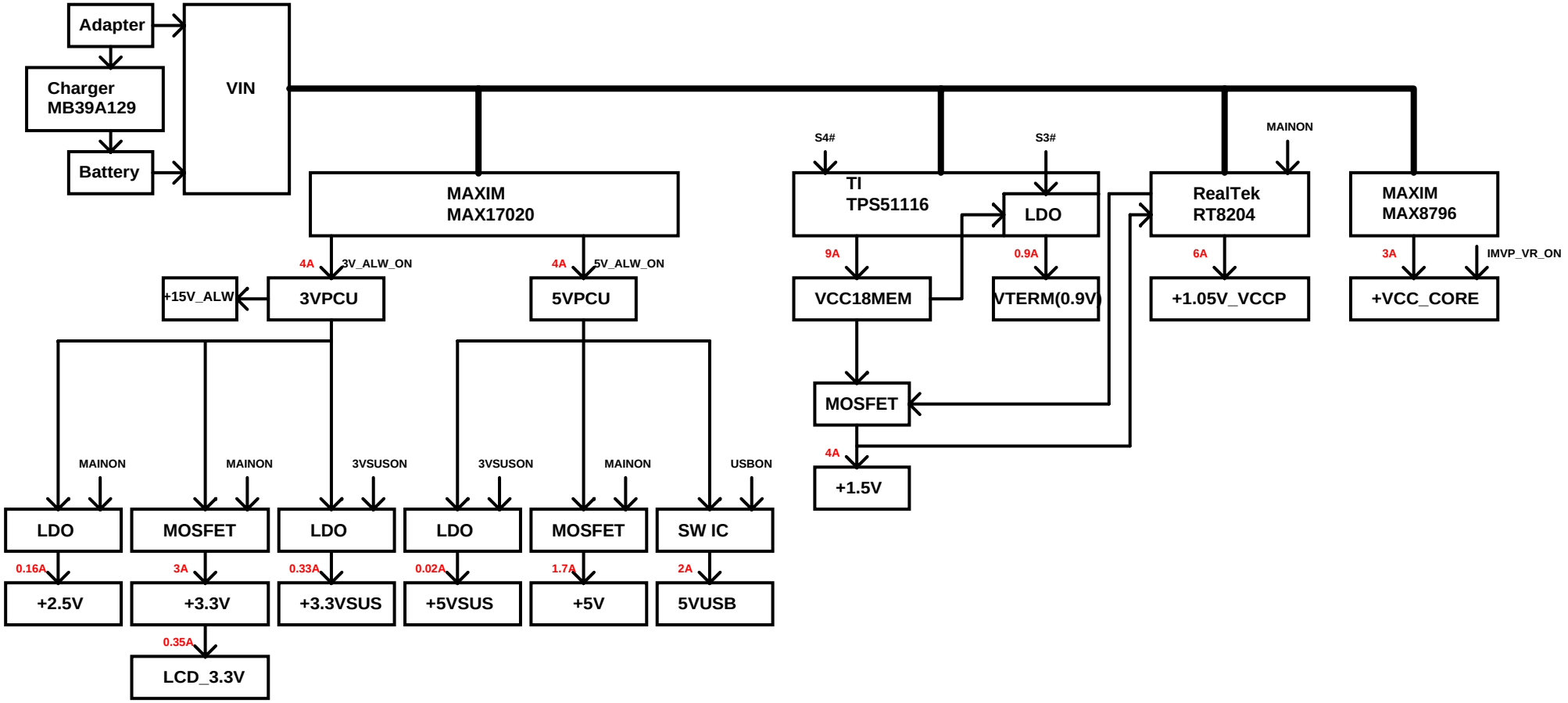
GND PLANE	PAGE	DESCRIPTION
GND	ALL	

Quanta Computer Inc.

PROJECT : FL1

Size	Document Number	Rev
	POWER MANAGER	1A

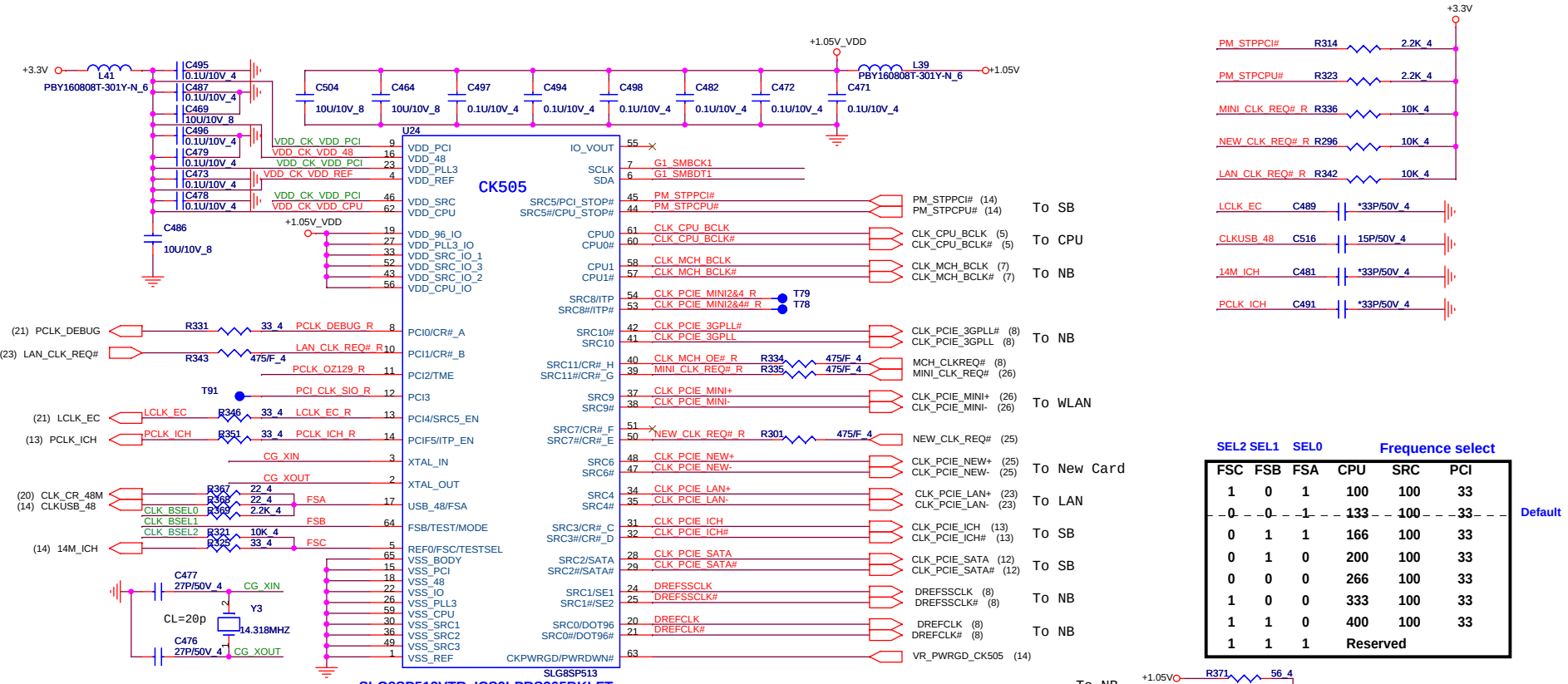
Date: Saturday, June 07, 2008 Sheet 2 of 34



Quanta Computer Inc.

PROJECT : FL1

Size	Document Number	Rev
	Power Block Diagram	1A
Date: Saturday, June 07, 2008	Sheet 3 of 34	



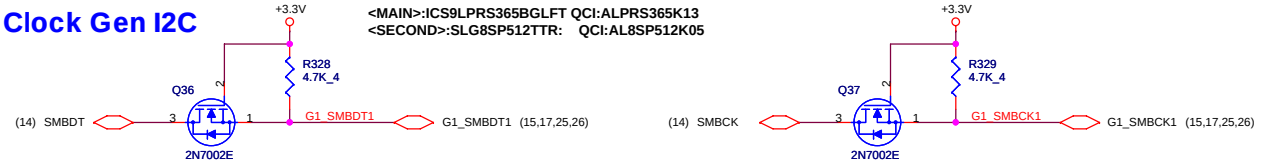
SEL2	SEL1	SEL0	CPU	SRC	PCI	
1	0	1	100	100	33	
0	0	1	133	100	33	Default
0	1	1	166	100	33	
0	1	0	200	100	33	
0	0	0	266	100	33	
1	0	0	333	100	33	
1	1	0	400	100	33	
1	1	1	Reserved			

Frequence select

	ICS9LPRS365 (ALPRS365K13)	RTM875T-606 (AL000875K06)	PULL HIGH	PULL DOWN
Pin 11	PCI2/TME	PCI2/TME internal PD	NO OVERCLOCKING (default)	NORMAL RUN
Pin 12	PCI-3	PCI-3/SRC5_EN internal PD	PIN37/38 IS SRC5	PIN37/38 IS PCI_STOP/CPU_STOP (default)
Pin 13	PCI-4/27M_SEL	PCI-4/27M_SEL internal PD	PIN 17/18 IS 27MHz	PIN 17/18 IS SRC/DOT (default)
Pin 14	PCIF-5/ITP_EN	PCIF-5/ITP_EN internal PD	PIN 46/47 IS CPUITP	PIN 46/47 IS SRC8 (default)

Clock Gen I2C

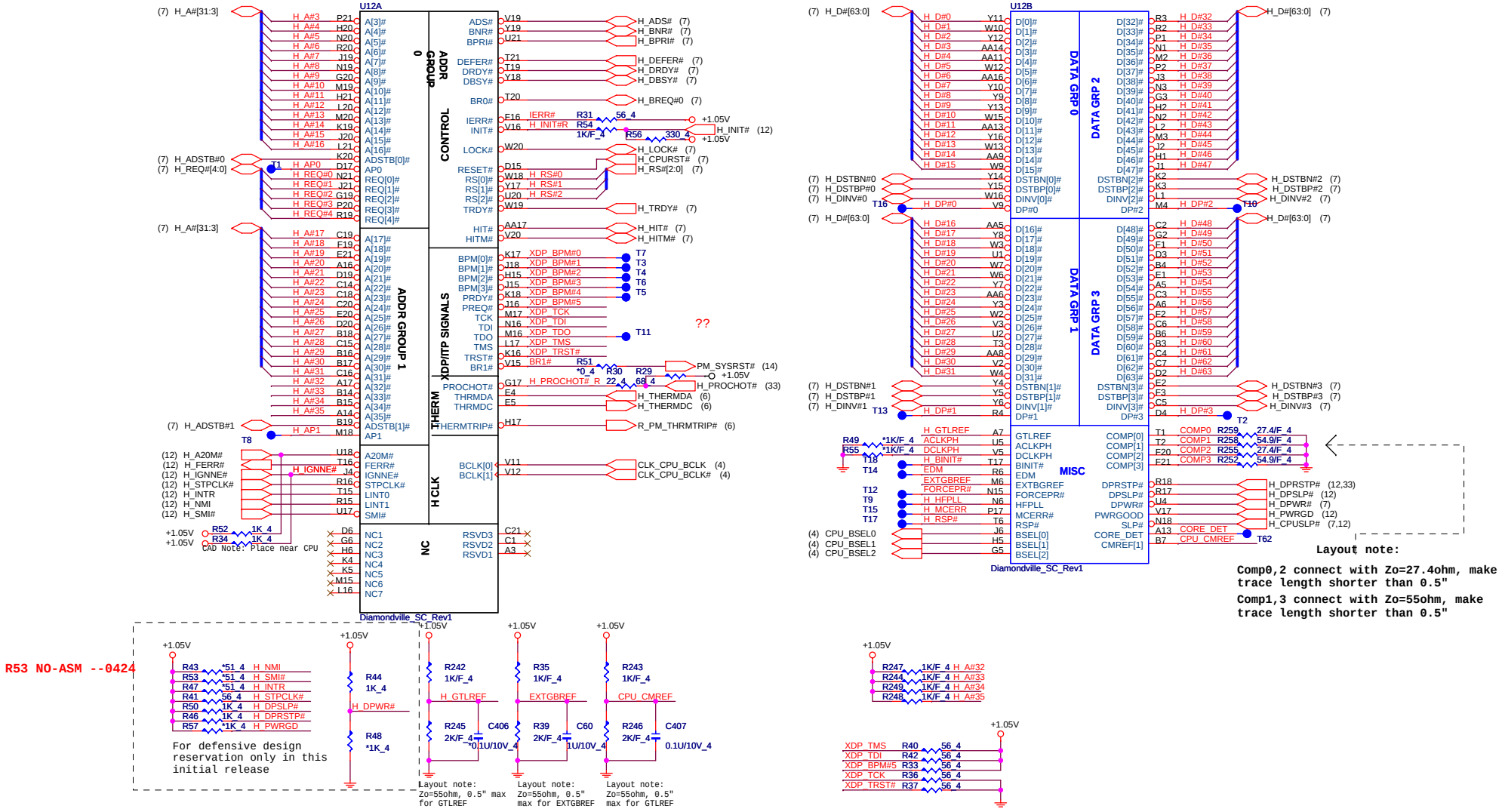
<MAIN>:ICS9LPRS365BGLFT QCI:ALPRS365K13
<SECOND>:SLG8SP512TTR: QCI:AL8SP512K05



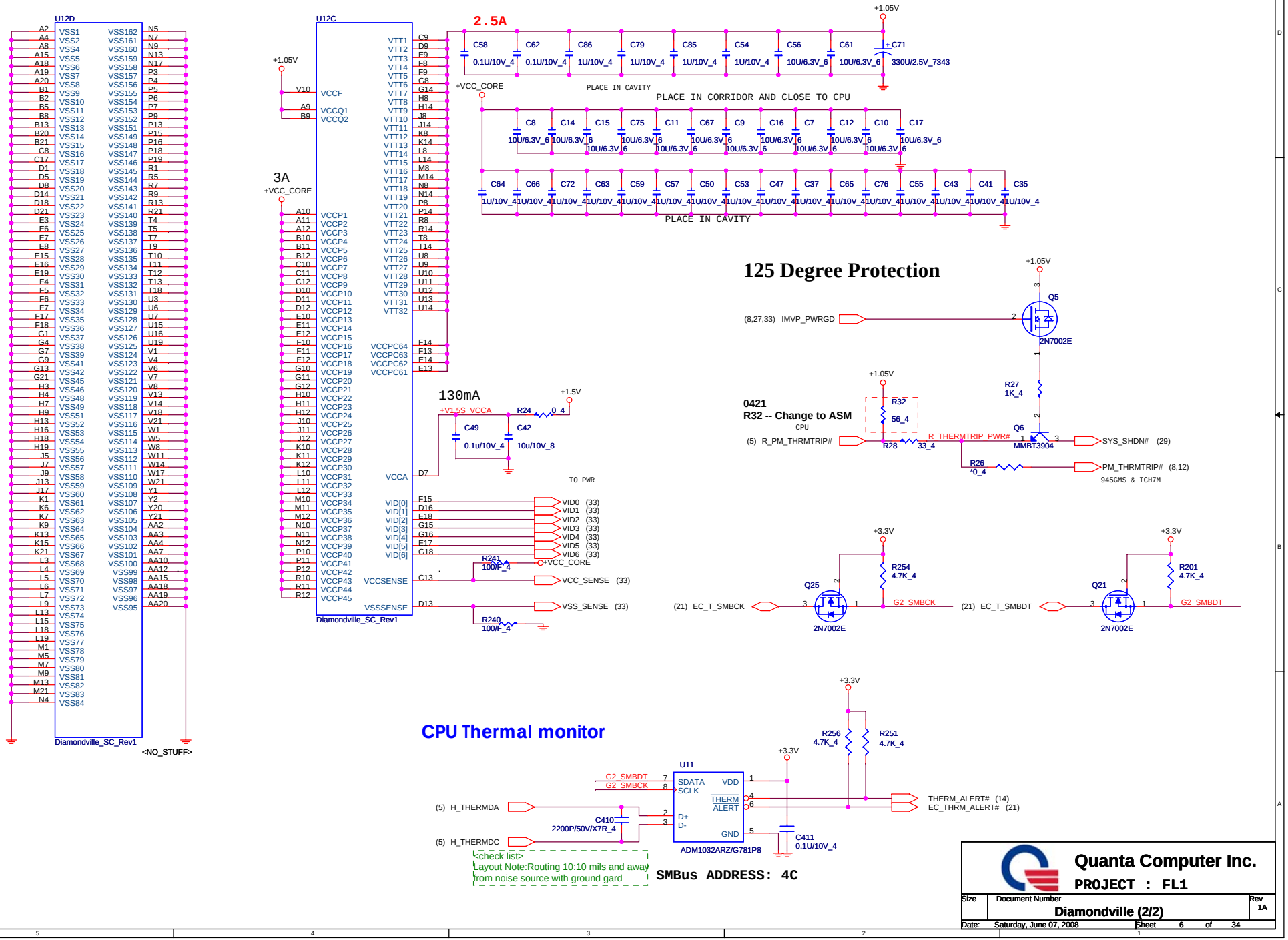
SM_BUS_Group1

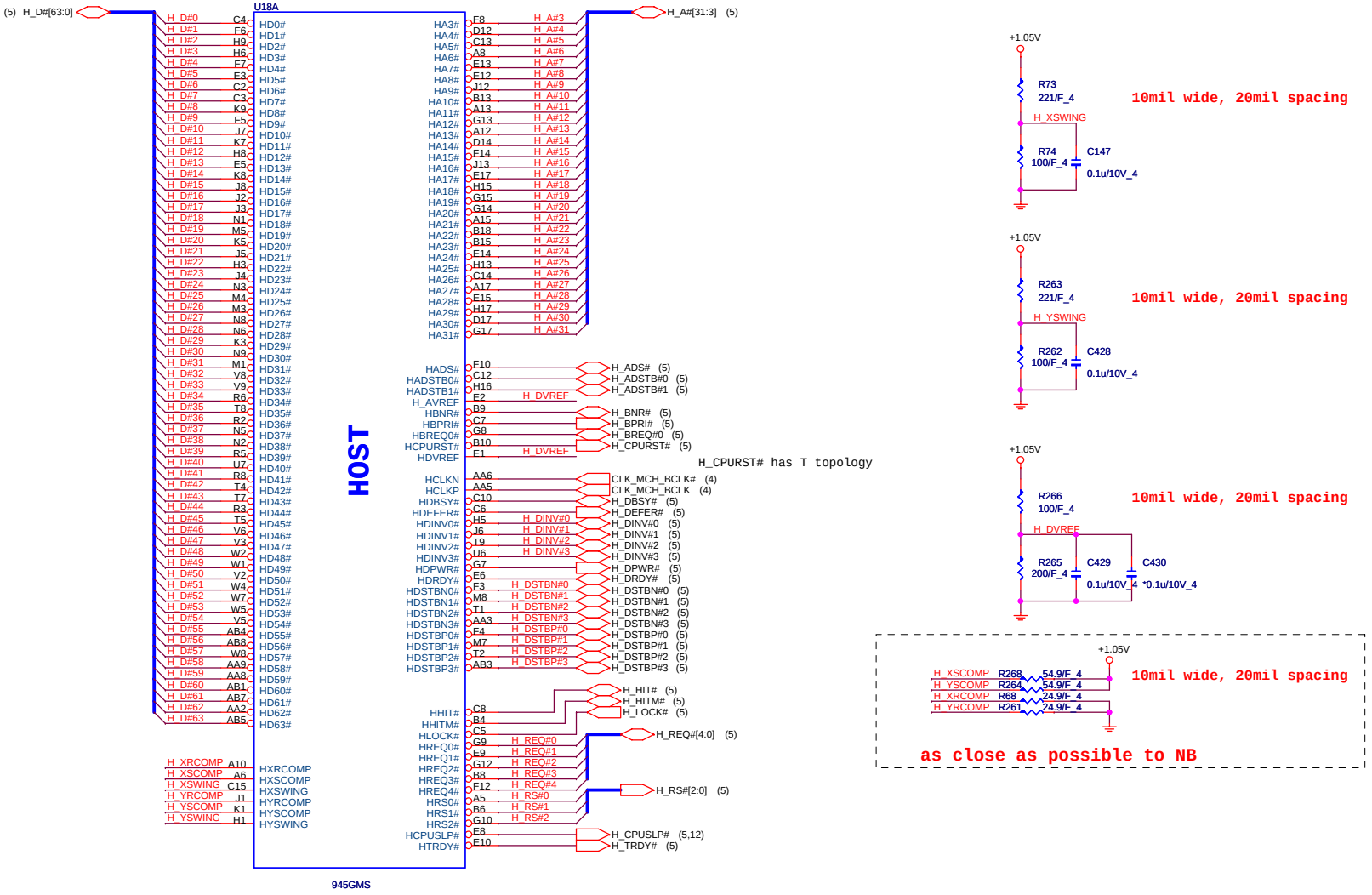
1. Clock Gen
2. Memory Rom
3. Mini Card
4. New Card
5. CPU Thermal Monitor

Quanta Computer Inc.			
PROJECT : FL1			
Size	Document Number: CLOCK GENERATOR		Rev 1A
Date: Saturday, June 07, 2008	Sheet 4 of 34		

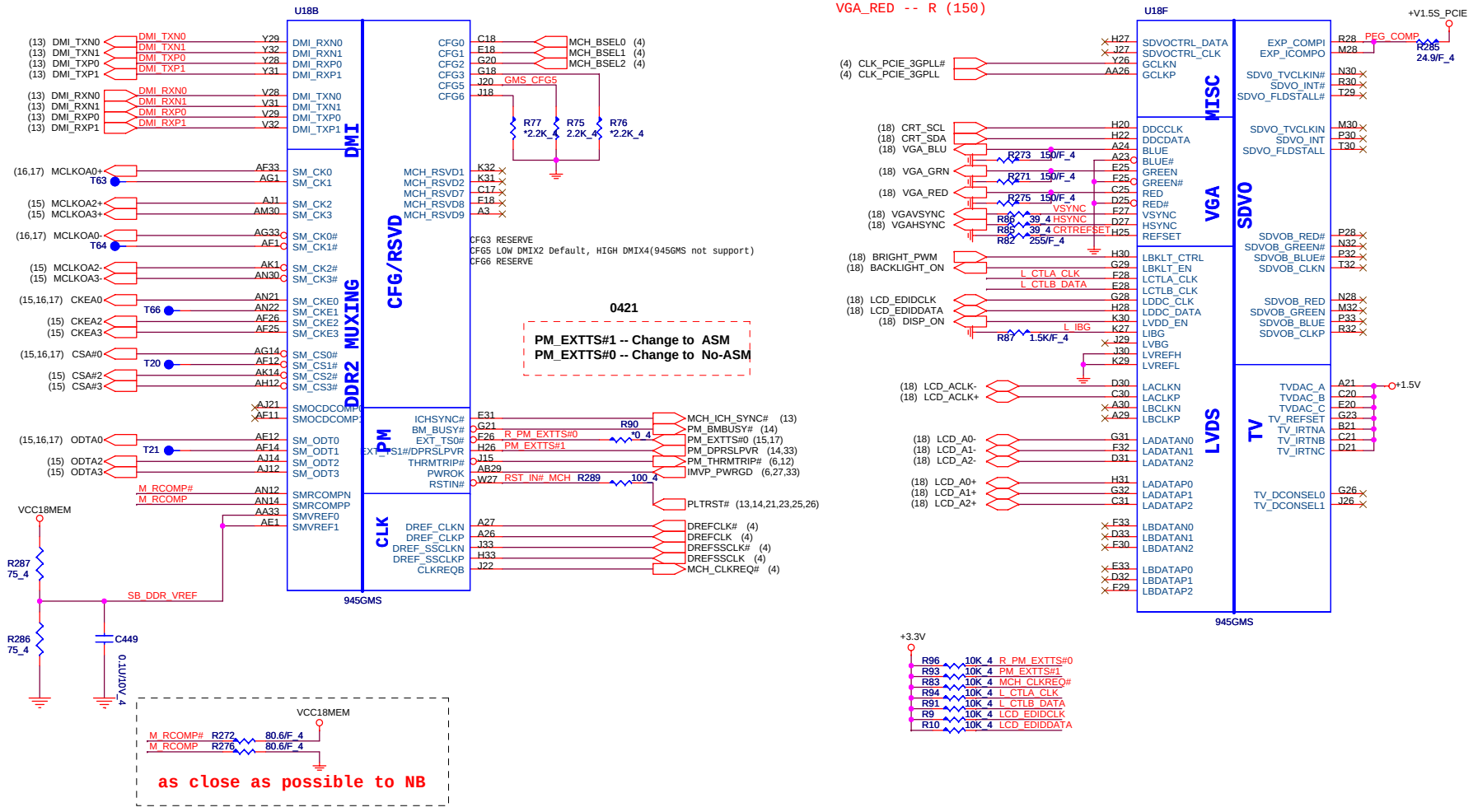


Quanta Computer Inc.			
PROJECT : FL1			
Size	Document Number		Rev
	Diamondville (1/2)		1A
Date: Saturday, June 07, 2008		Sheet 5 of 34	

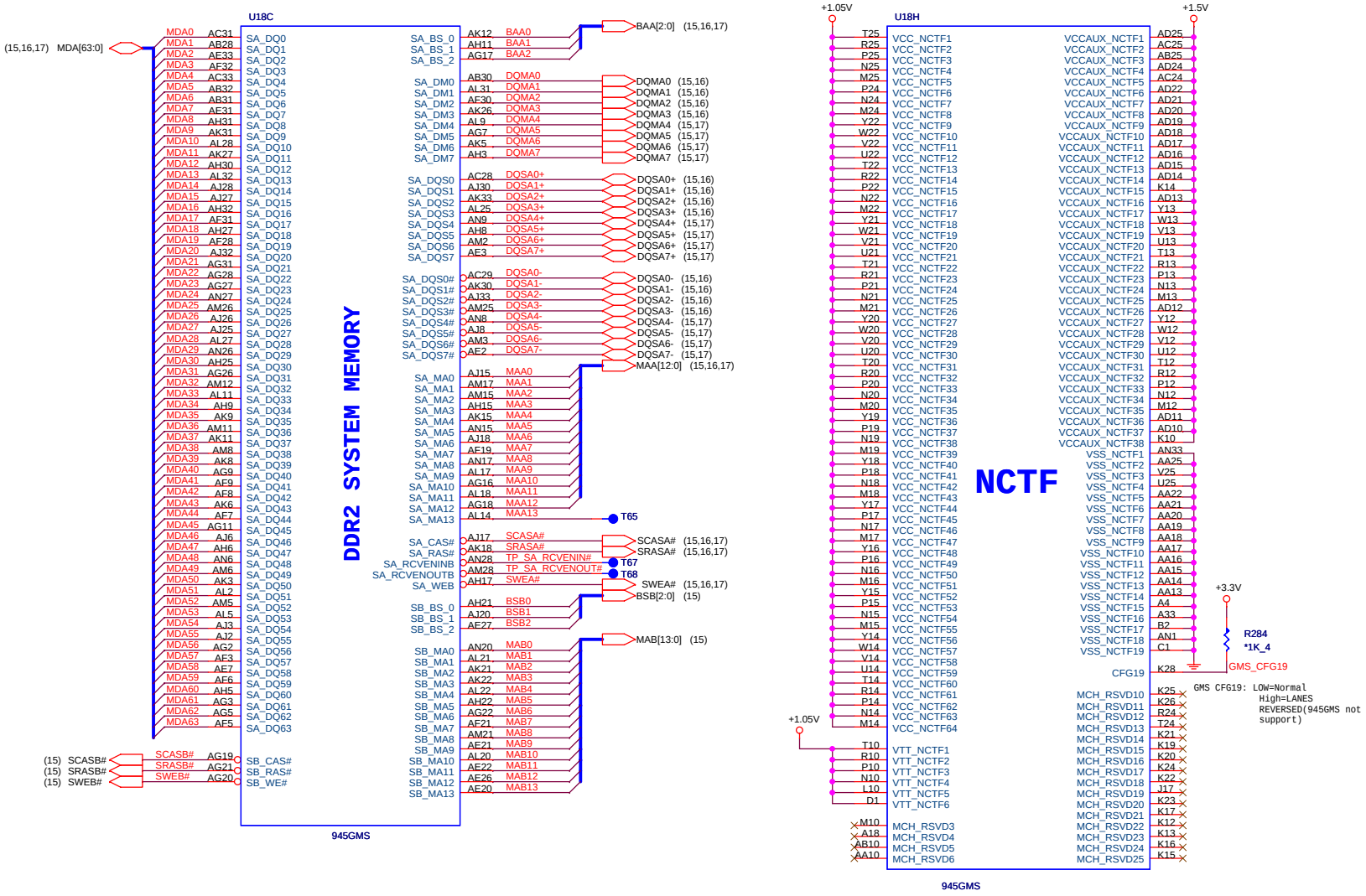

U12D
Diamondville_SC_Rev1
<NO_STUFF>
U12C
Diamondville_SC_Rev1
2.5A
+1.05V
PLACE IN CAVITY
PLACE IN CORRIDOR AND CLOSE TO CPU
3A
+VCC_CORE
130mA
+1.5V
TO PWR
125 Degree Protection
0421
R32 -- Change to ASM
CPU
2N7002E
MMBT3904
SYS_SHDN# (29)
PM_THRMTRIP# (8,12)
945GMS & ICH7M
CPU Thermal monitor
U11
ADM1032ARZ/G781P8
SMBus ADDRESS: 4C
<check list>
Layout Note:Routing 10:10 mils and away from noise source with ground gard
Quanta Computer Inc.
PROJECT : FL1
Diamondville (2/2)
Rev 1A
Saturday, June 07, 2008
Sheet 6 of 34



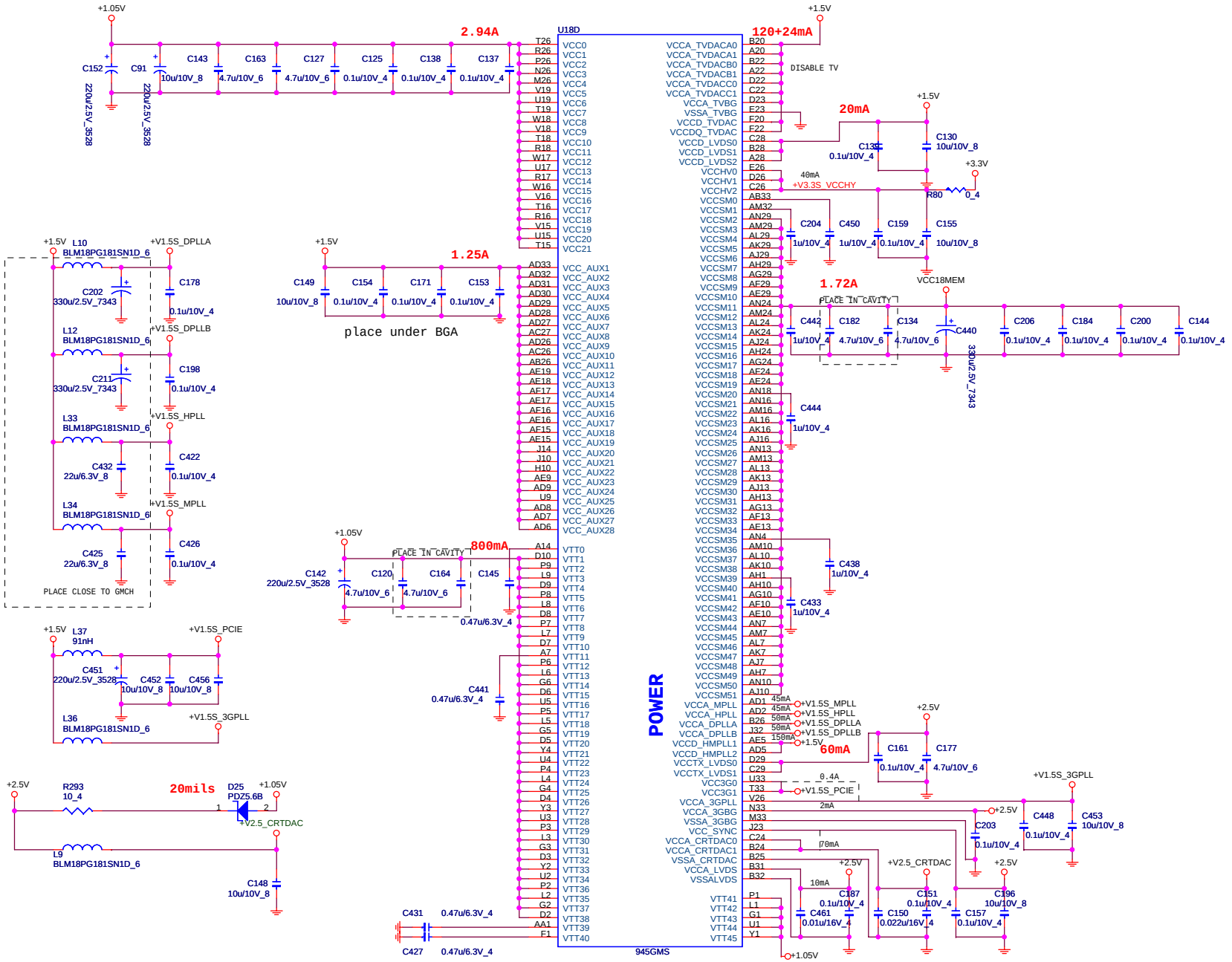
Quanta Computer Inc.	
PROJECT : FL1	
Size	Document Number: 945GMS_HOST — Rev: 1A
Date: Saturday, June 07, 2008	Sheet 7 of 34



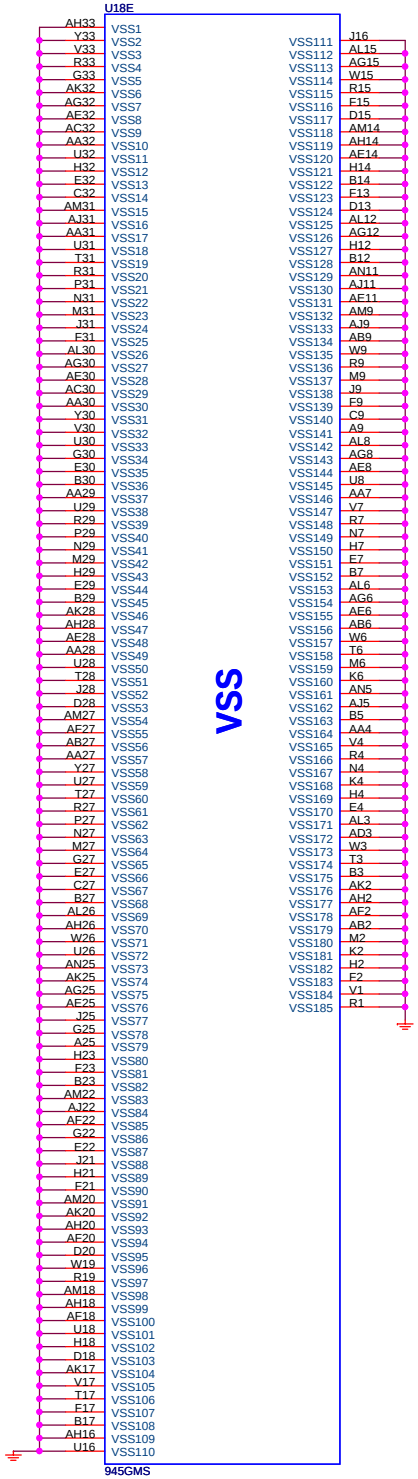
Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number	Rev
	945GMS_LVDS, DMI, DDR CLK	1A
Date: Saturday, June 07, 2008	Sheet 8 of 34	

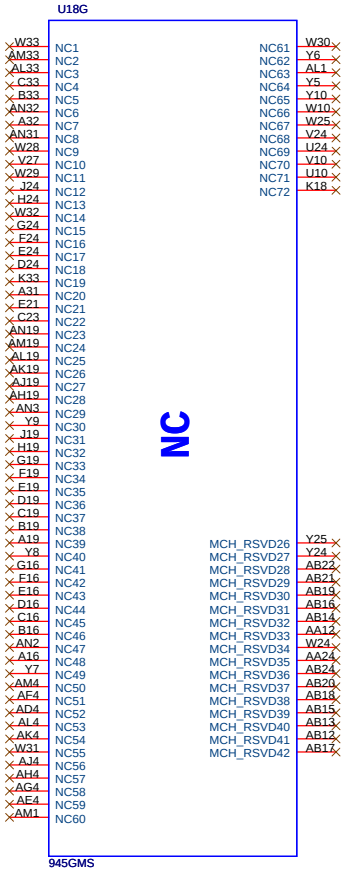


Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number	Rev
	945GMS_DDRII	1A
Date: Saturday, June 07, 2008	Sheet 9 of 34	

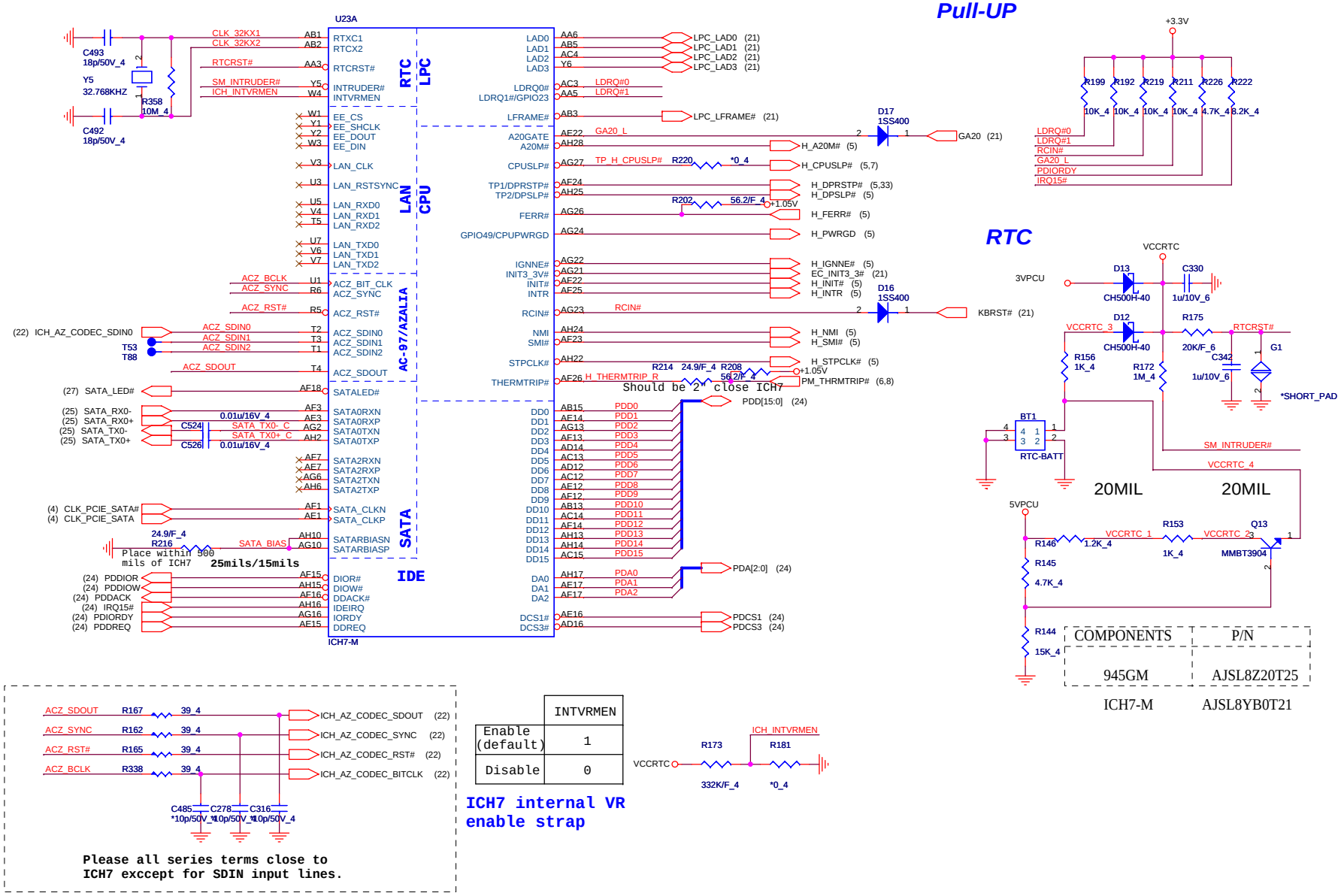


Quanta Computer Inc.			
PROJECT : FL1			
Size	Document Number		Rev
	945GMS_POWER		1A
Date: Saturday, June 07, 2008		Sheet 10 of 34	





Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number: 945GMS_GND	Rev: 1A
Date: Saturday, June 07, 2008	Sheet 11 of 34	



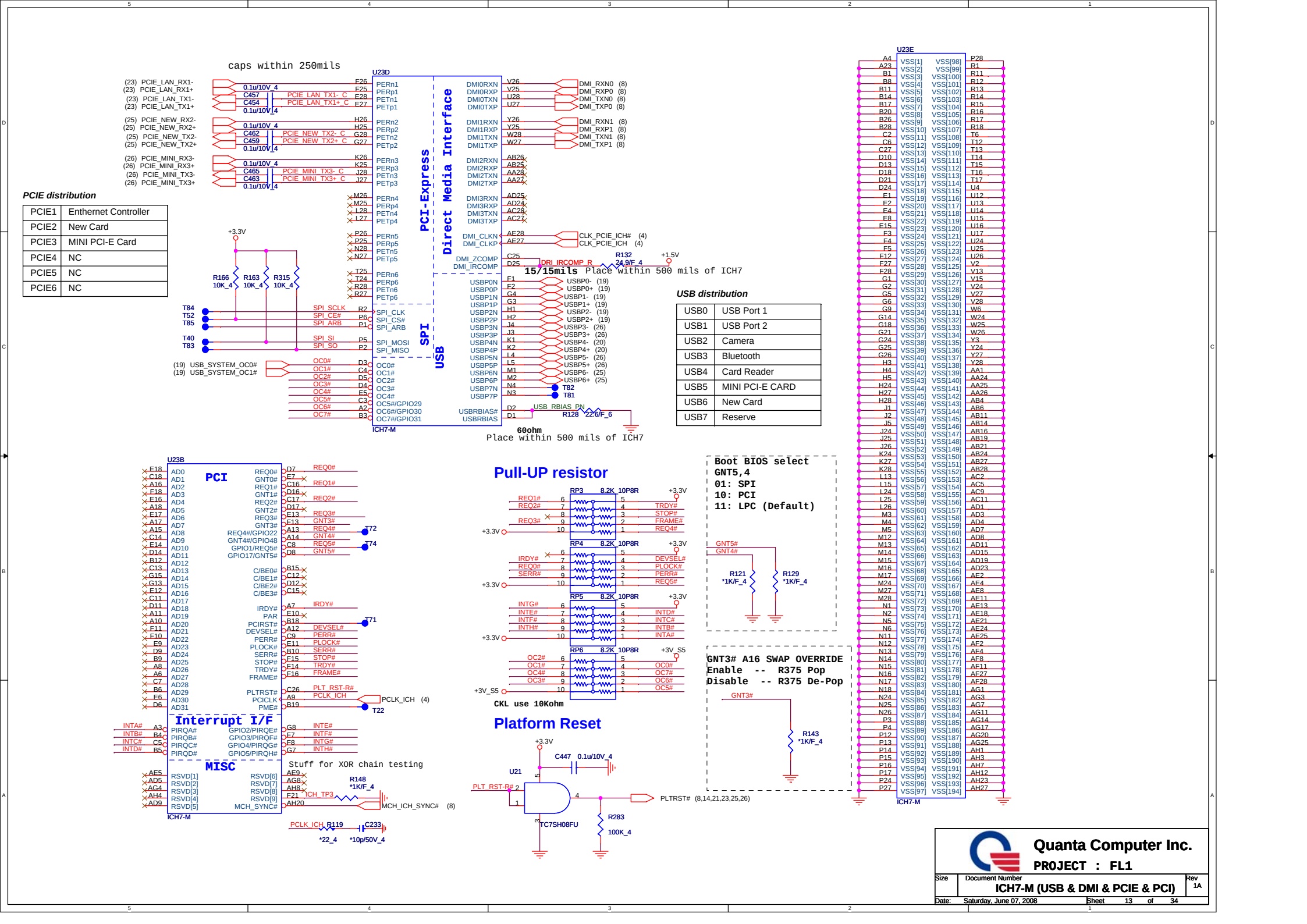
COMPONENTS	P/N
945GM	AJSL8Z20T25
ICH7-M	AJSL8YB0T21

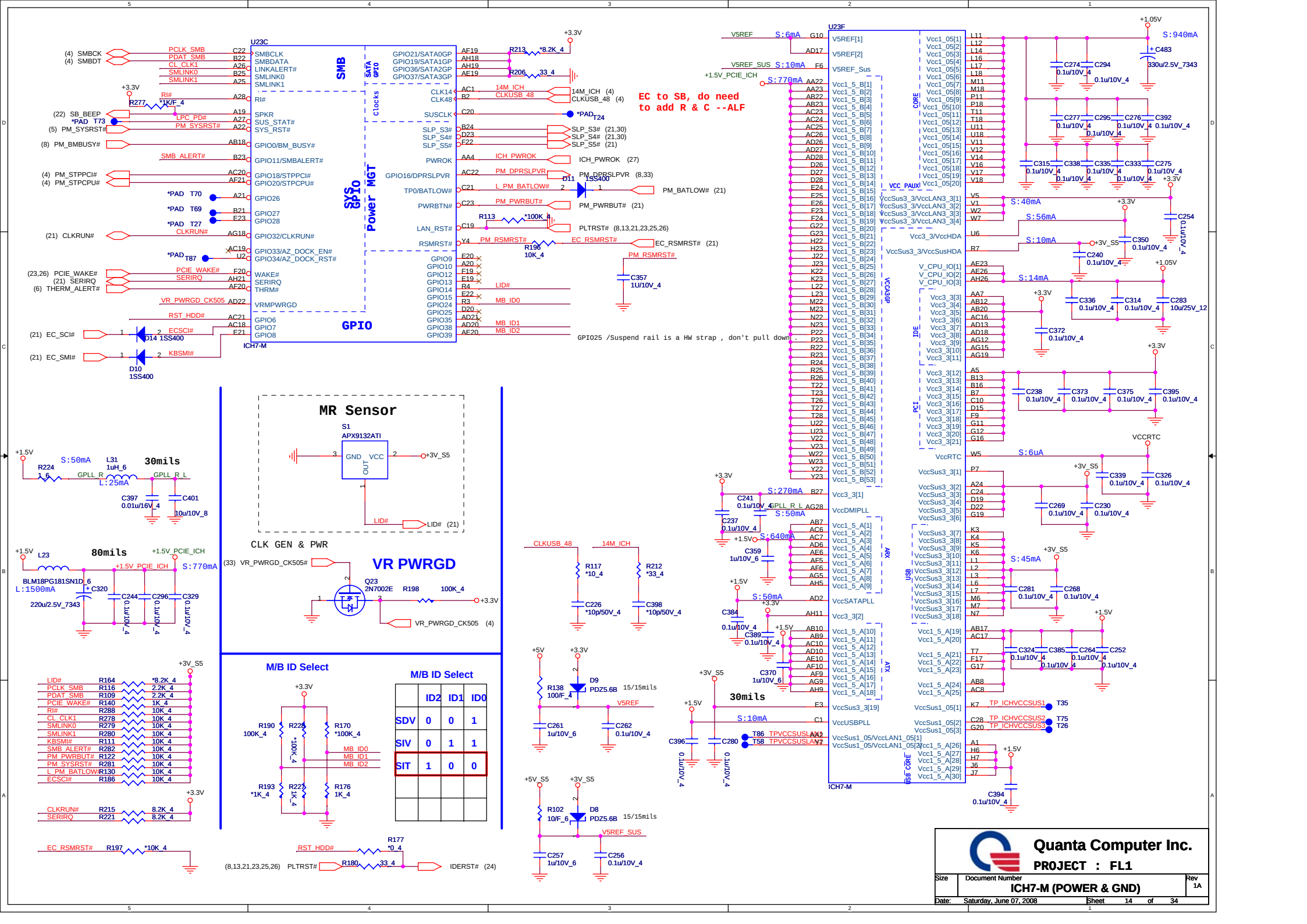
Quanta Computer Inc.

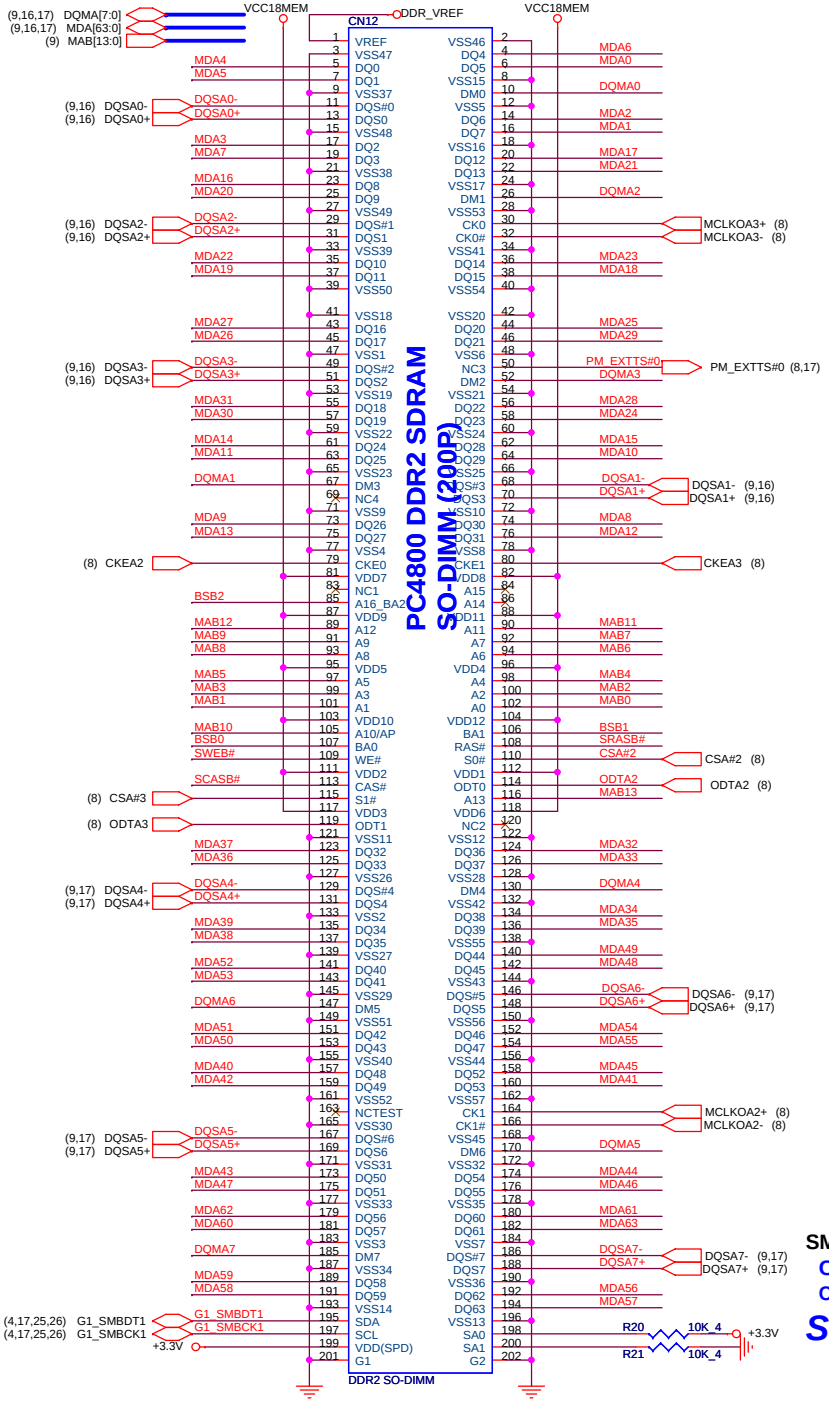
PROJECT : FL1

Size	Document Number	Rev
	ICH7-M (CPU, SATA, IDE,LPC)	1A

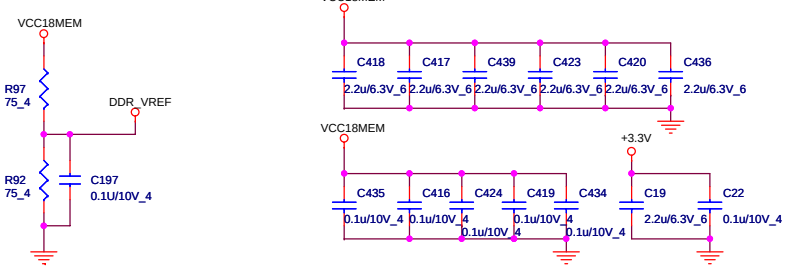
Date: Saturday, June 07, 2008 Sheet 12 of 34


caps within 250mils
U23D
PCI-Express
Direct Media Interface
SPI
USB
ICH7-M
PCIE distribution
PCIE1 Enthernet Controller
PCIE2 New Card
PCIE3 MINI PCI-E Card
PCIE4 NC
PCIE5 NC
PCIE6 NC
USB distribution
USB0 USB Port 1
USB1 USB Port 2
USB2 Camera
USB3 Bluetooth
USB4 Card Reader
USB5 MINI PCI-E CARD
USB6 New Card
USB7 Reserve
15/15mils Place within 500 mils of ICH7
60ohm
Place within 500 mils of ICH7
U23E
U23B
PCI
Interrupt I/F
MISC
Stuff for XOR chain testing
Pull-UP resistor
CKL use 10Kohm
Platform Reset
Boot BIOS select
GNT5,4
01: SPI
10: PCI
11: LPC (Default)
GNT3# A16 SWAP OVERRIDE
Enable -- R375 Pop
Disable -- R375 De-Pop
Quanta Computer Inc.
PROJECT : FL1
ICH7-M (USB & DMI & PCIE & PCI)
Size
Document Number
Rev 1A
Date: Saturday, June 07, 2008
Sheet 13 of 34

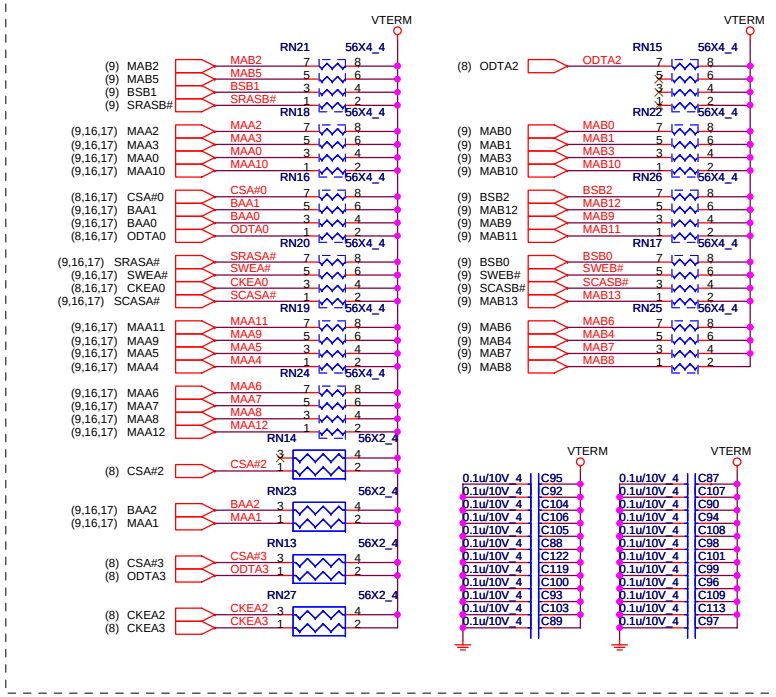

U23C
SMB
SATA GPIO
Clocks
SYS GPIO
Power MGT
GPIO
ICH7-M
SMBCLK
SMBDATA
LINKALERT#
SMLINK0
SMLINK1
RI#
SPKR
SUS_STAT#
SYS_RST#
GPIO0/BM_BUSY#
GPIO11/SMBALERT#
GPIO18/STPPCI#
GPIO20/STPCPU#
GPIO26
GPIO27
GPIO28
GPIO32/CLKRUN#
GPIO33/AZ_DOCK_EN#
GPIO34/AZ_DOCK_RST#
WAKE#
SERIRQ
THRM#
VRMPWRGD
GPIO6
GPIO7
GPIO8
GPIO21/SATA0GP
GPIO19/SATA1GP
GPIO36/SATA2GP
GPIO37/SATA3GP
CLK14
CLK48
SUSCLK
SLP_S3#
SLP_S4#
SLP_S5#
PWROK
GPIO16/DPRSLPVR
TP0/BATLOW#
PWRBTN#
LAN_RST#
RSMRST#
GPIO9
GPIO10
GPIO12
GPIO13
GPIO14
GPIO15
GPIO24
GPIO25
GPIO35
GPIO38
GPIO39
(4) SMBCK
(4) SMBDT
PCLK_SMB
PDAT_SMB
CL_CLK1
SMLINK0
SMLINK1
(22) SB_BEEP
*PAD T73
(5) PM_SYSRST#
(8) PM_BMBUSY#
SMB_ALERT#
(4) PM_STPPCI#
(4) PM_STPCPU#
*PAD T70
*PAD T69
*PAD T27
(21) CLKRUN#
*PAD T87
(23,26) PCIE_WAKE#
(21) SERIRQ
(6) THERM_ALERT#
VR_PWRGD_CK505
RST_HDD#
(21) EC_SCI#
(21) EC_SMI#
D14 1SS400
D10 1SS400
ECSCI#
KBSMI#
LPC_PD#
PM_SYSRST#
CLKRUN#
PCIE_WAKE#
SERIRQ
14M_ICH
CLKUSB_48
*PAD T24
SLP_S3# (21,30)
SLP_S4# (21,30)
SLP_S5# (21)
ICH_PWROK
PM_DPRSLPVR
L_PM_BATLOW#
PM_PWRBUT#
PLTRST# (8,13,21,23,25,26)
PM_RSMRST#
EC_RSMRST#
LID#
MB_ID0
MB_ID1
MB_ID2
R213 *8.2K_4
R206 33_4
R113 *100K_4
R196 10K_4
D11 1SS400
C357 1U/10V_4
EC to SB, do need to add R & C --ALF
GPIO25 /Suspend rail is a HW strap , don't pull down .
PM_BATLOW# (21)
PM_PWRBUT# (21)
PLTRST# (8,13,21,23,25,26)
EC_RSMRST# (21)
MR Sensor
S1
APX9132ATI
GND
VCC
OUT
+3V_S5
LID# (21)
CLK GEN & PWR
VR PWRGD
(33) VR_PWRGD_CK505#
Q23
2N7002E
R198 100K_4
VR_PWRGD_CK505 (4)
M/B ID Select
M/B ID Select
ID2 ID1 ID0
SDV 0 0 1
SIV 0 1 1
SIT 1 0 0
R190 100K_4
R225 *100K_4
R170 *100K_4
R193 *1K_4
R227 1K_4
R176 1K_4
MB_ID0
MB_ID1
MB_ID2
LID# R164 *8.2K_4
PCLK_SMB R116 2.2K_4
PDAT_SMB R109 2.2K_4
PCIE_WAKE# R140 1K_4
RI# R288 10K_4
CL_CLK1 R278 10K_4
SMLINK0 R279 10K_4
SMLINK1 R280 10K_4
KBSMI# R111 10K_4
SMB_ALERT# R282 10K_4
PM_PWRBUT# R122 10K_4
PM_SYSRST# R281 10K_4
L_PM_BATLOW# R130 10K_4
ECSCI# R186 10K_4
CLKRUN# R215 8.2K_4
SERIRQ R221 8.2K_4
EC_RSMRST# R197 *10K_4
RST_HDD# R177 *0_4
(8,13,21,23,25,26) PLTRST# R180 33_4 IDERST# (24)
R224 1_6 GPLL_R L:25mA GPLL_R_L
S:50mA L31 1uH_6 30mils
C397 0.01u/16V_4 C401 10u/10V_8
L23 80mils +1.5V_PCIE_ICH S:770mA
BLM18PG181SN1D_6 L:1500mA C320 220u/2.5V_7343
C244 C296 C329 0.1u/10V_4
R117 *10_4 R212 *33_4
C226 *10p/50V_4 C398 *10p/50V_4
CLKUSB_48 14M_ICH
R138 100/F_4 D9 PDZ5.6B 15/15mils V5REF
C261 1u/10V_6 C262 0.1u/10V_4
R102 10/F_6 D8 PDZ5.6B 15/15mils V5REF_SUS
C257 1u/10V_6 C256 0.1u/10V_4
U23F
V5REF S:6mA
V5REF_SUS S:10mA
+1.5V_PCIE_ICH S:770mA
CORE
VCC_PAUX
VCCAGP
IDE
PCI
AGP
USB
ATX
USB CORE
ICH7-M
C241 0.1u/10V_4 GPLL_R_L S:270mA
C237 0.1u/10V_4 S:50mA
S:640mA C359 1u/10V_6
S:50mA C384 0.1u/10V_4
C389 0.1u/10V_4
C370 1u/10V_6
30mils S:10mA
C396 0.1u/10V_4 C280 0.1u/10V_4
T86 TP_VCCSUSLAN1
T58 TP_VCCSUSLAN2
S:940mA C483 330u/2.5V_7343
C274 C294 C277 C295 C276 C392 C315 C338 C335 C333 C275 0.1u/10V_4
S:40mA C254
S:56mA
S:10mA C350 C240
S:14mA C336 C314 C283 10u/25V_12
C372
C238 C373 C375 C395
VCCRTC S:6uA C339 C326
C269 C230
S:45mA C281 C268
C324 C385 C264 C252
C394
TP_ICHVCCSUS1 T35
TP_ICHVCCSUS2 T75
TP_ICHVCCSUS3 T26
Quanta Computer Inc.
PROJECT : FL1
Size
Document Number
ICH7-M (POWER & GND)
Rev 1A
Date: Saturday, June 07, 2008
Sheet 14 of 34



Close to SO-DIMM



Termination resistor



SMbus address A1

CLOCK 1,2

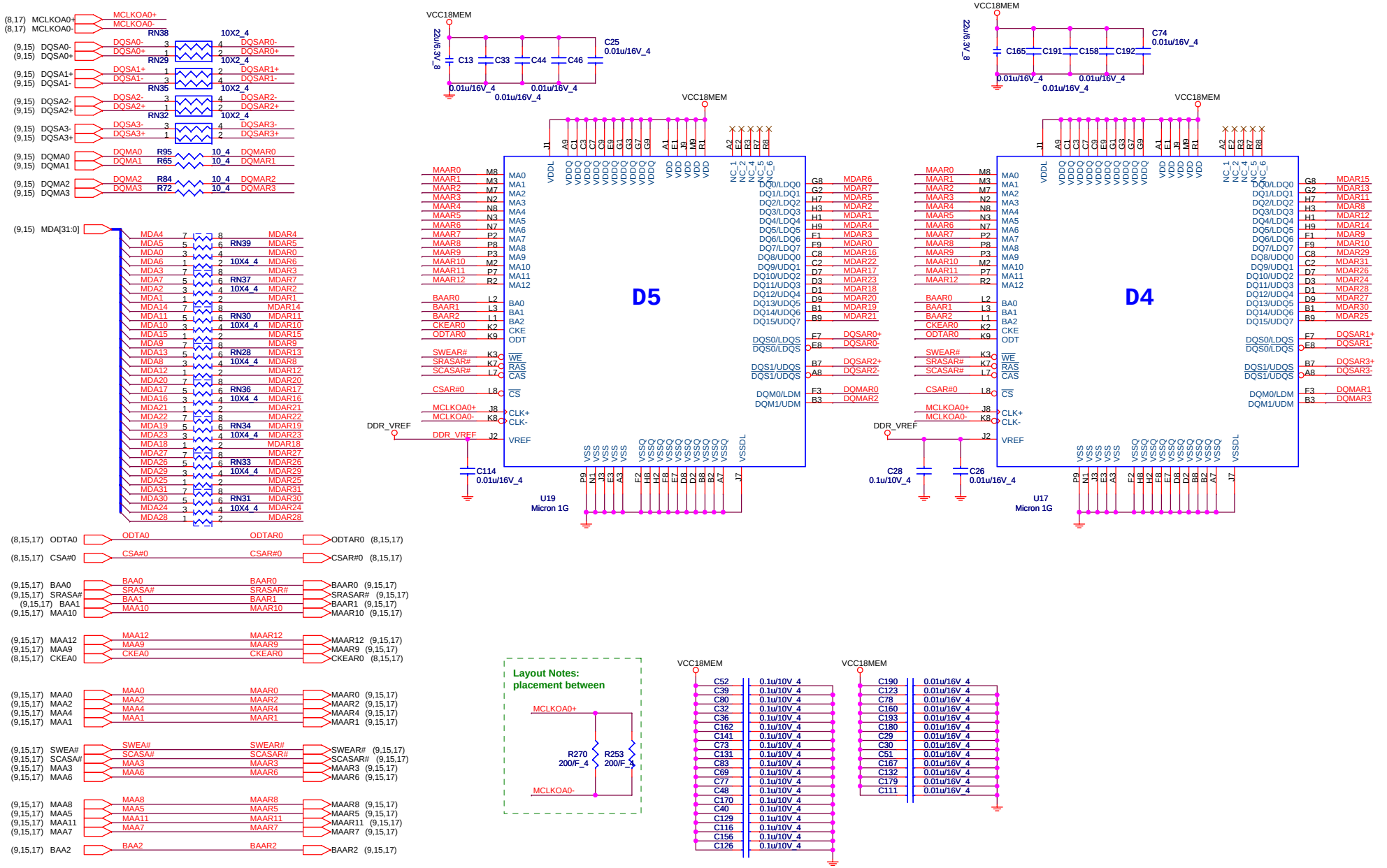
CKE 2,3

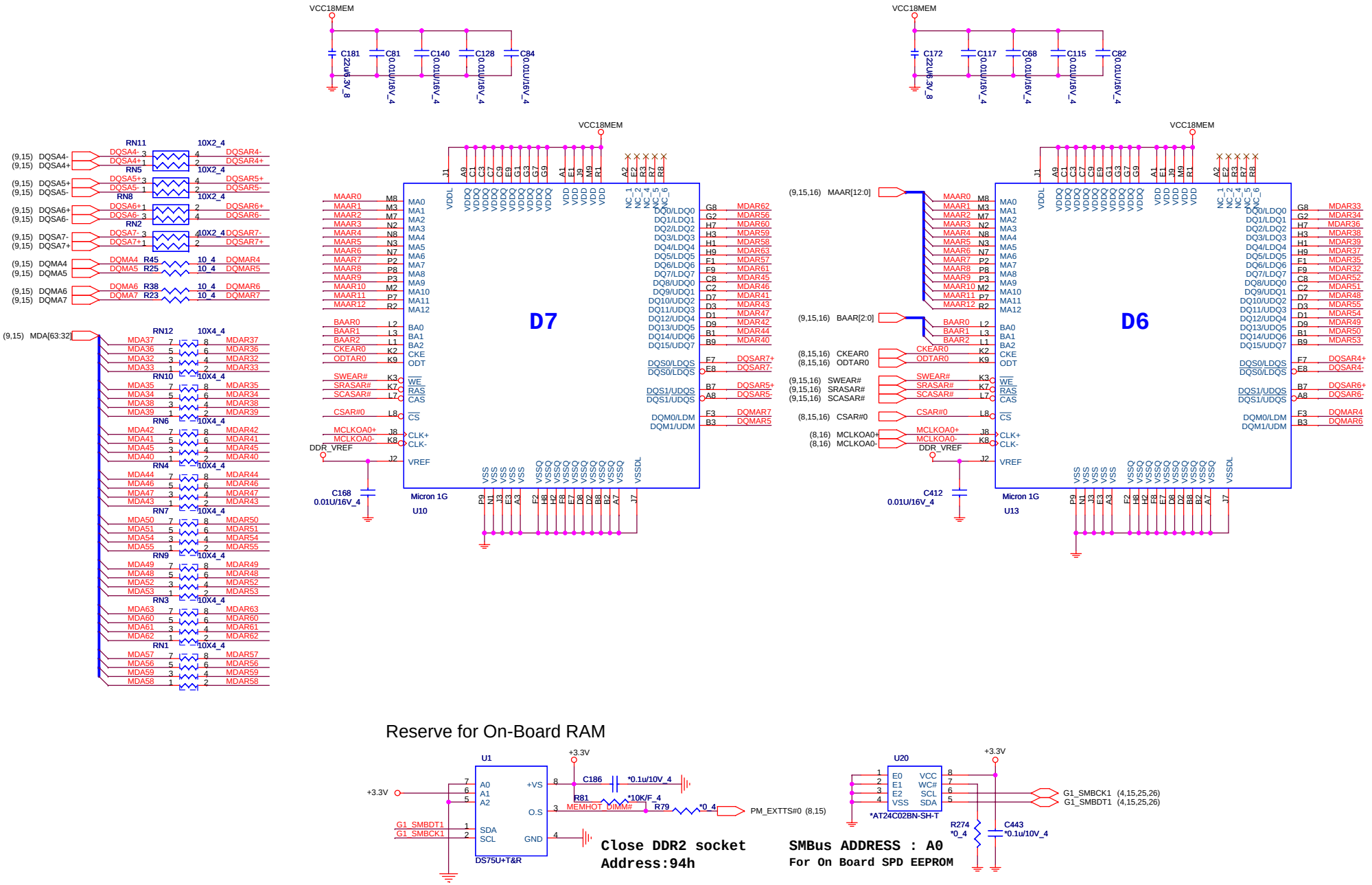
Standard Type H: 5.2mm

Quanta Computer Inc.

PROJECT : FL1

Size	Document Number	Rev
	DDR2 SO-DIMM(200P)	1A
Date: Saturday, June 07, 2008	Sheet 15 of 34	


(8,17) MCLKOA0+
(8,17) MCLKOA0-
MCLKOA0+
MCLKOA0-
RN38
10X2_4
(9,15) DQSA0-
(9,15) DQSA0+
DQSA0-
DQSA0+
DQSAR0-
DQSAR0+
RN29
(9,15) DQSA1+
(9,15) DQSA1-
RN35
(9,15) DQSA2-
(9,15) DQSA2+
RN32
(9,15) DQSA3-
(9,15) DQSA3+
(9,15) DQMA0
(9,15) DQMA1
(9,15) DQMA2
(9,15) DQMA3
R95
R65
R84
R72
10_4
DQMAR0
DQMAR1
DQMAR2
DQMAR3
(9,15) MDA[31:0]
RN39
RN37
RN30
RN28
RN36
RN34
RN33
RN31
10X4_4
(8,15,17) ODTA0
ODTAR0
(8,15,17) CSA#0
CSAR#0
(9,15,17) BAA0
(9,15,17) SRASA#
(9,15,17) BAA1
(9,15,17) MAA10
BAA0
SRASA#
BAA1
MAA10
BAAR0
SRASAR#
BAAR1
MAAR10
(9,15,17) MAA12
(9,15,17) MAA9
(8,15,17) CKEA0
MAA12
MAA9
CKEA0
MAAR12
MAAR9
CKEAR0
(9,15,17) MAA0
(9,15,17) MAA2
(9,15,17) MAA4
(9,15,17) MAA1
MAA0
MAA2
MAA4
MAA1
MAAR0
MAAR2
MAAR4
MAAR1
(9,15,17) SWEA#
(9,15,17) SCASA#
(9,15,17) MAA3
(9,15,17) MAA6
SWEA#
SCASA#
MAA3
MAA6
SWEAR#
SCASAR#
MAAR3
MAAR6
(9,15,17) MAA8
(9,15,17) MAA5
(9,15,17) MAA11
(9,15,17) MAA7
MAA8
MAA5
MAA11
MAA7
MAAR8
MAAR5
MAAR11
MAAR7
(9,15,17) BAA2
BAA2
BAAR2
VCC18MEM
22u/6.3V_8
C13
C33
C44
C46
C25
0.01u/16V_4
D5
D4
U19
Micron 1G
U17
Micron 1G
DDR_VREF
C114
C28
0.1u/10V_4
C26
C165
C191
C158
C192
C74
Layout Notes:
placement between
MCLKOA0+
MCLKOA0-
R270
200/F_4
R253
C52
C39
C80
C32
C36
C162
C141
C73
C131
C83
C69
C77
C48
C170
C40
C129
C116
C156
C126
C190
C123
C78
C160
C193
C180
C29
C30
C51
C167
C132
C179
C111
Quanta Computer Inc.
PROJECT : FL1
Size
Document Number
DDR2_MODULE_RANK1
Rev
1A
Date: Saturday, June 07, 2008
Sheet 16 of 34



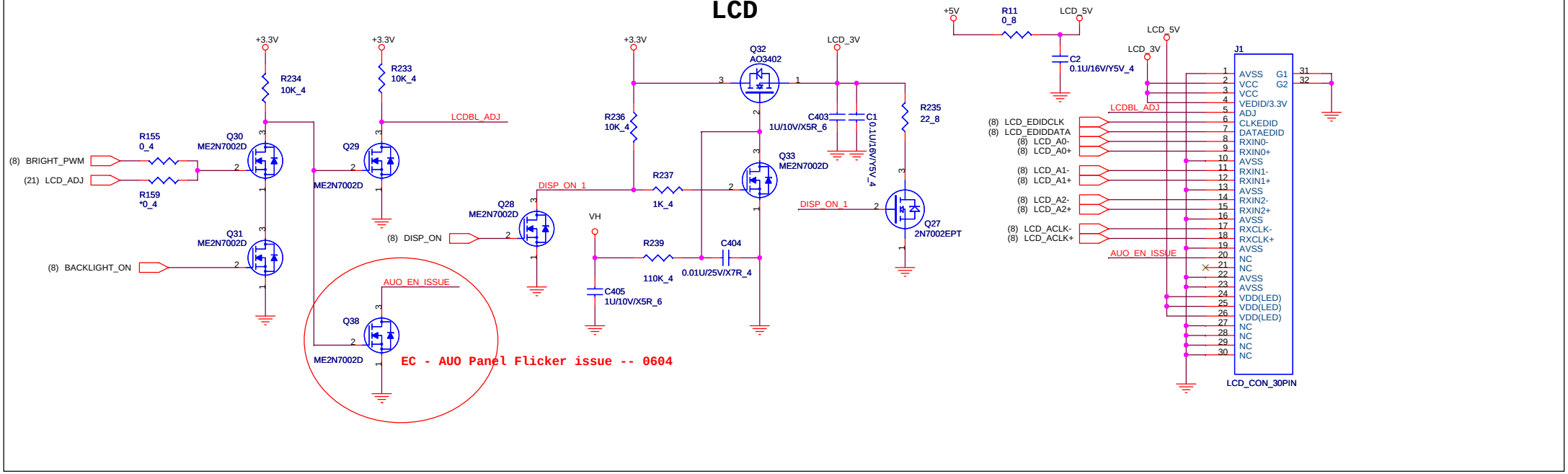
Quanta Computer Inc.

PROJECT : FL1

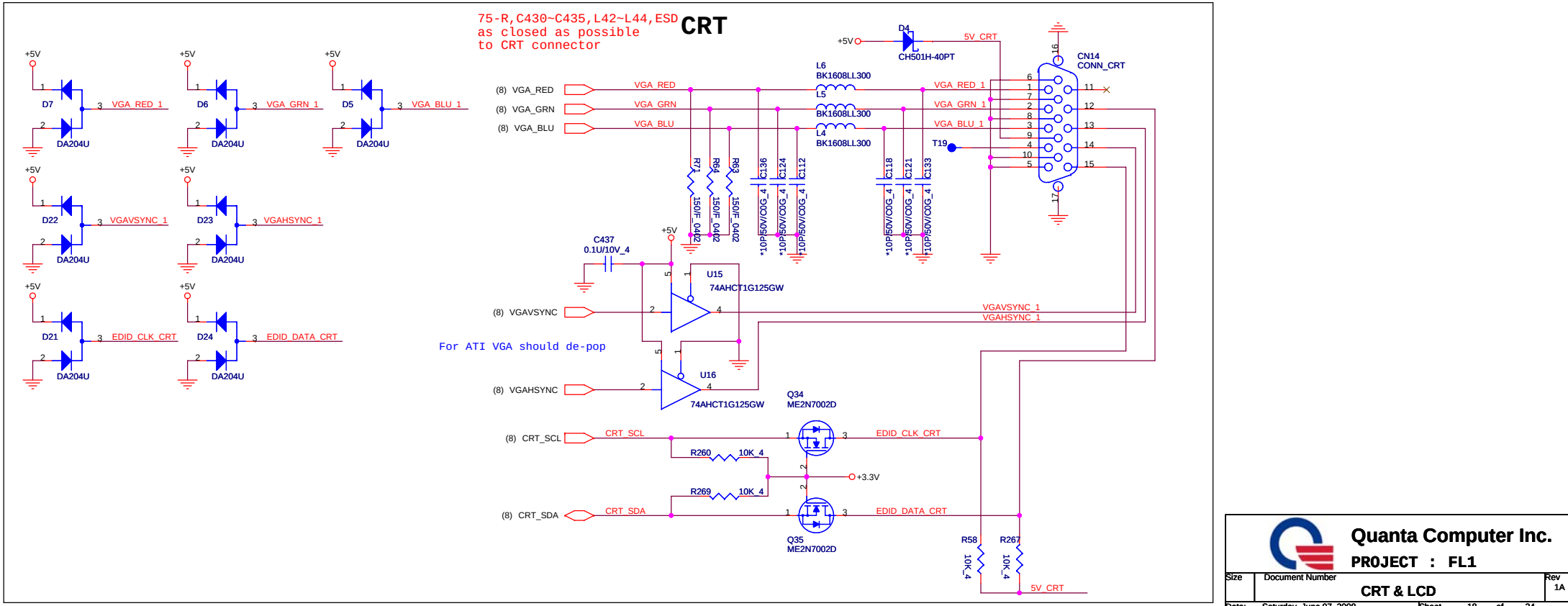
Size	Document Number	Rev
	DDR2_MODULE_RANK2	1A

Date: Saturday, June 07, 2008 Sheet 17 of 34

LCD

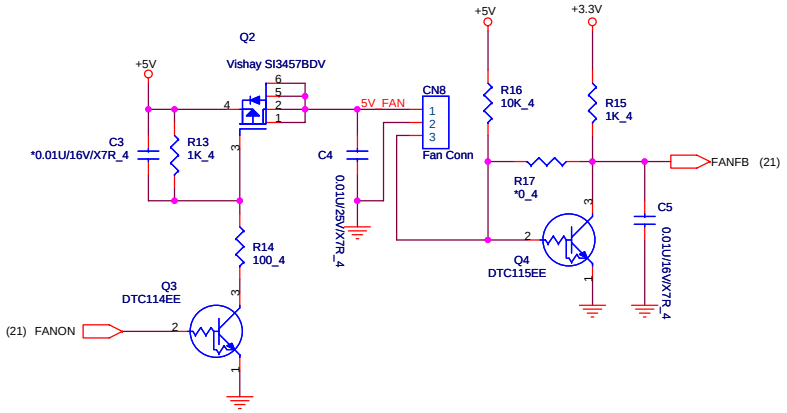


CRT

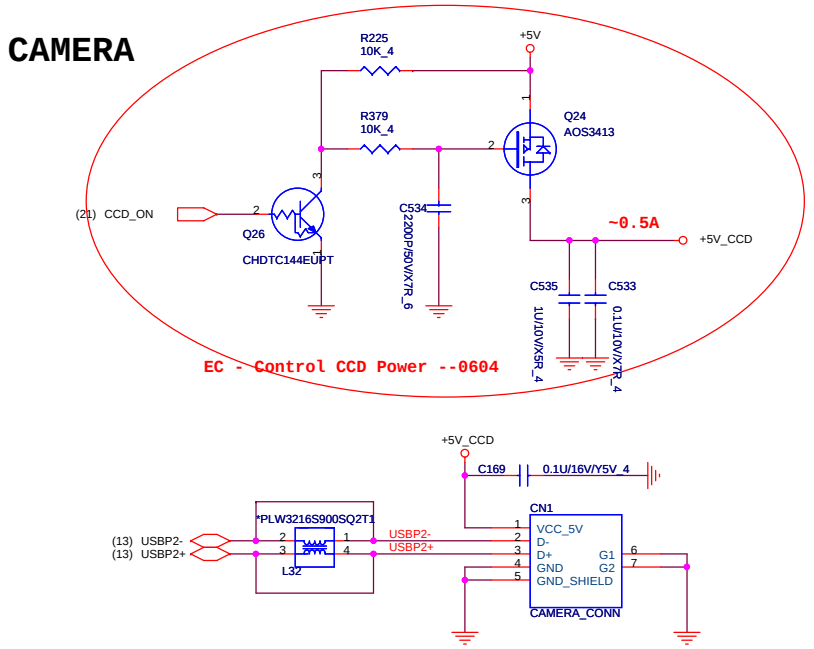


Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number: CRT & LCD	Rev: 1A
Date: Saturday, June 07, 2008	Sheet 18 of 34	

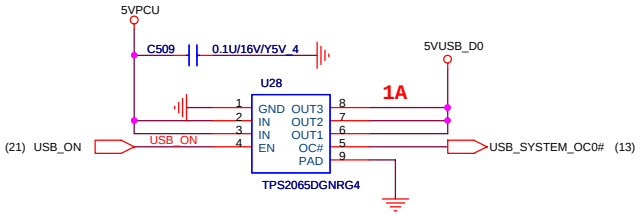
FAN

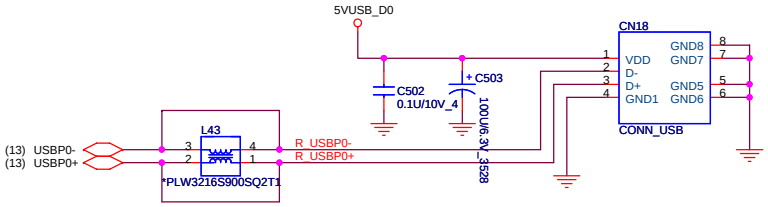


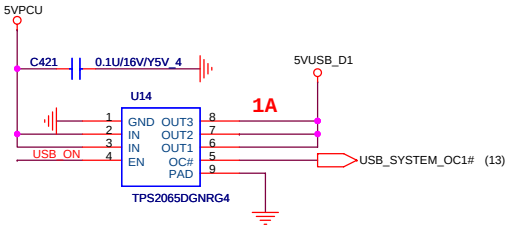
CAMERA

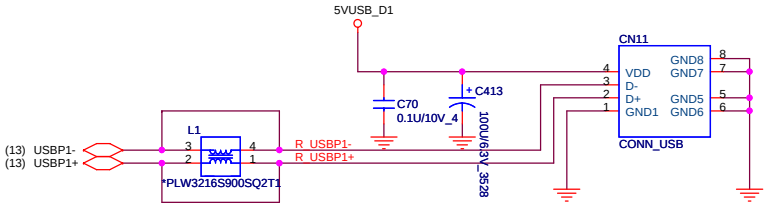


USB X 2 PORTS





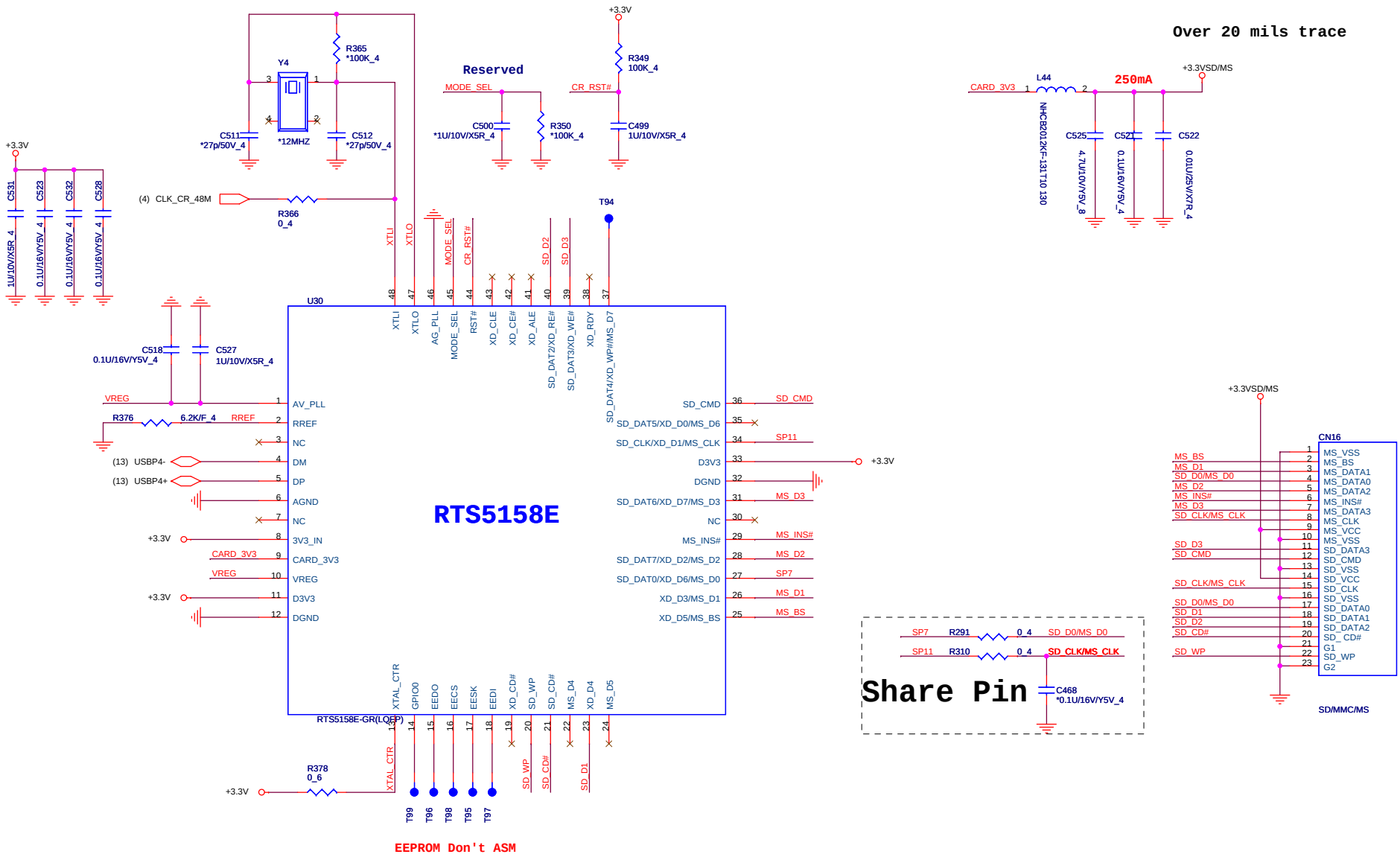




Quanta Computer Inc.

PROJECT : FL1

Size	Document Number	Rev
	USB & FAN & CAMERA	1A
Date: Saturday, June 07, 2008	Sheet 19 of 34	



Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number: CARD READER RTL5158E	Rev: 1A
Date: Saturday, June 07, 2008	Sheet 20 of 34	

SPI mode hard strap(must)

HW STRAP OPTION

KSO3 ON : ISP mode

Pull-Down resistor required to avoid leakage current

Decouping

CLOSE TO CHIP

WLAN SW_ How to used?

Change SW Type 0425

KEYBOARD

KB CONN -- 26PIN

EMI Request

KBID = H : JP

KBID = L : US & UK

SPI FLASH ROM

W25X80AVSSIG

USB to SPI writer Connector to update EC F/W

(Low Forward Voltage Vf <= 0.4V)

*NA/HEADER 4X2 (Pitch 2.54mm)

R36
POP : Normal
DEPOP : Update EC F/W

FOR DEBUG

TOUCH PAD CONNECTOR

Normal_ SW#_ How to used?

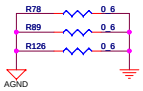
FOR DEBUG

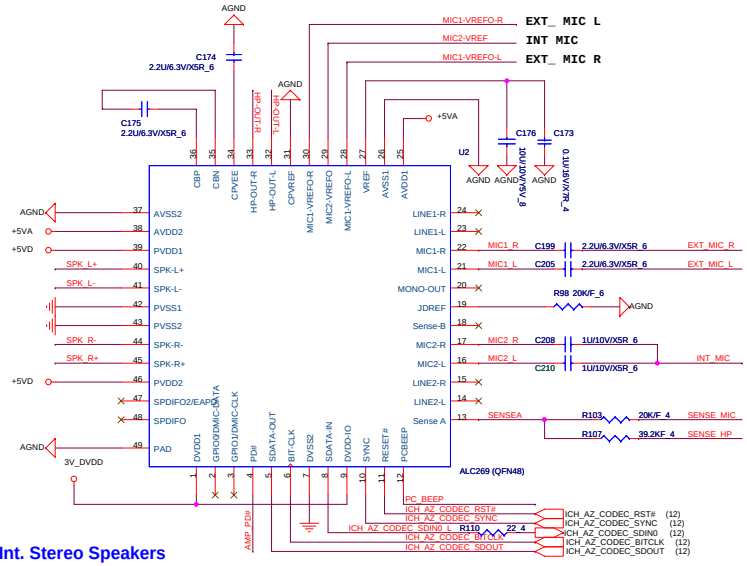
*Debug Conn

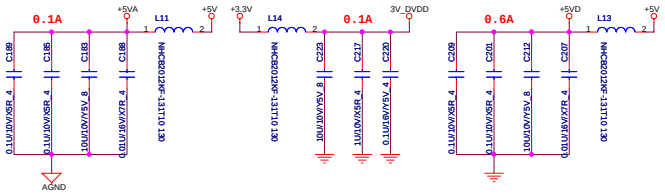
Quanta Computer Inc.

PROJECT : FL1

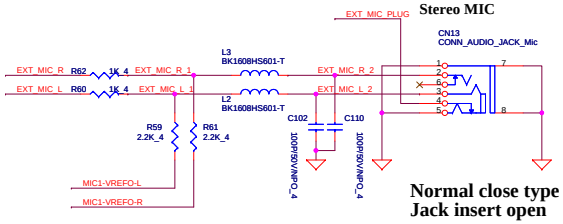
Size	Document Number	Rev
	EC KB3310	1A
Date: Saturday, June 07, 2008	Sheet 21 of 34	



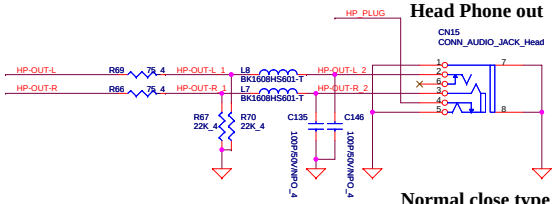




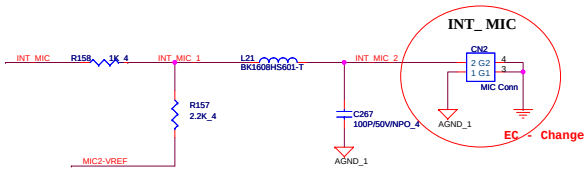
External Microphone Jack



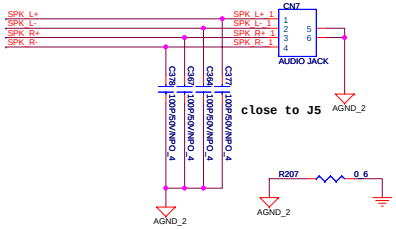
Normal close type
Jack insert open



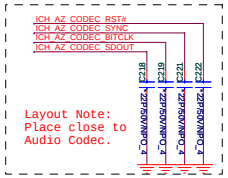
Normal close type
Jack insert open



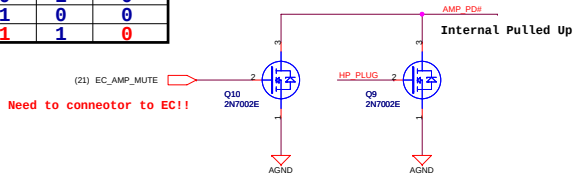
EC - Change MIC Type with wire-- 0604

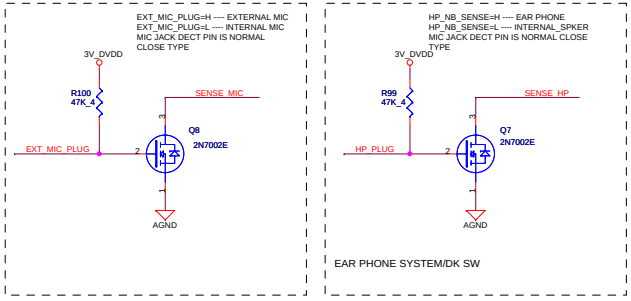


Int. Stereo Speakers
5V / 4 Ohm / 1.5W

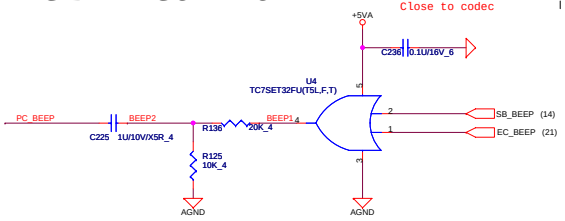


EC_AMP_MUTE	HP_PLUG	AMP_SHDN#
0	0	1
0	1	0
1	0	0
1	1	0

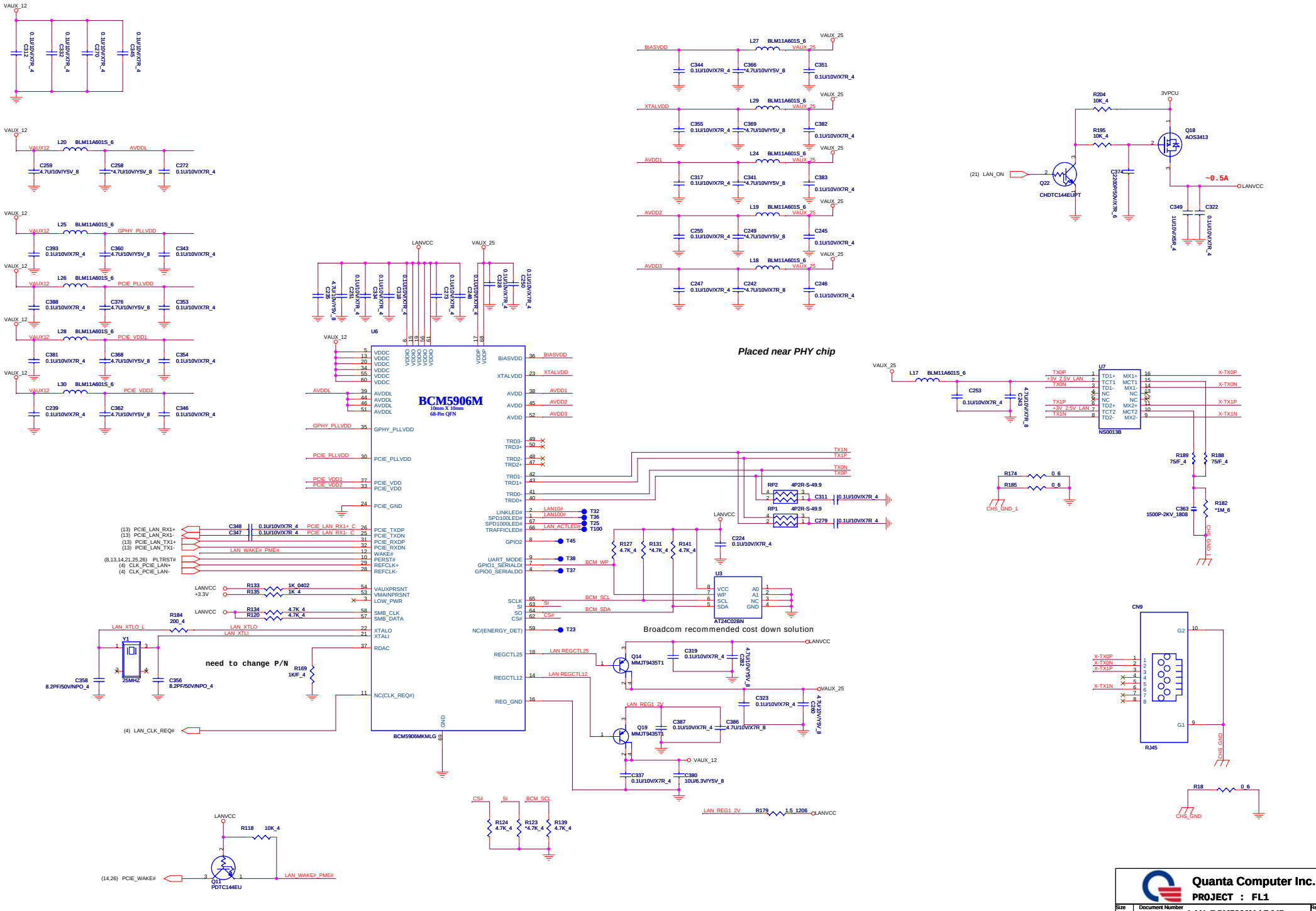


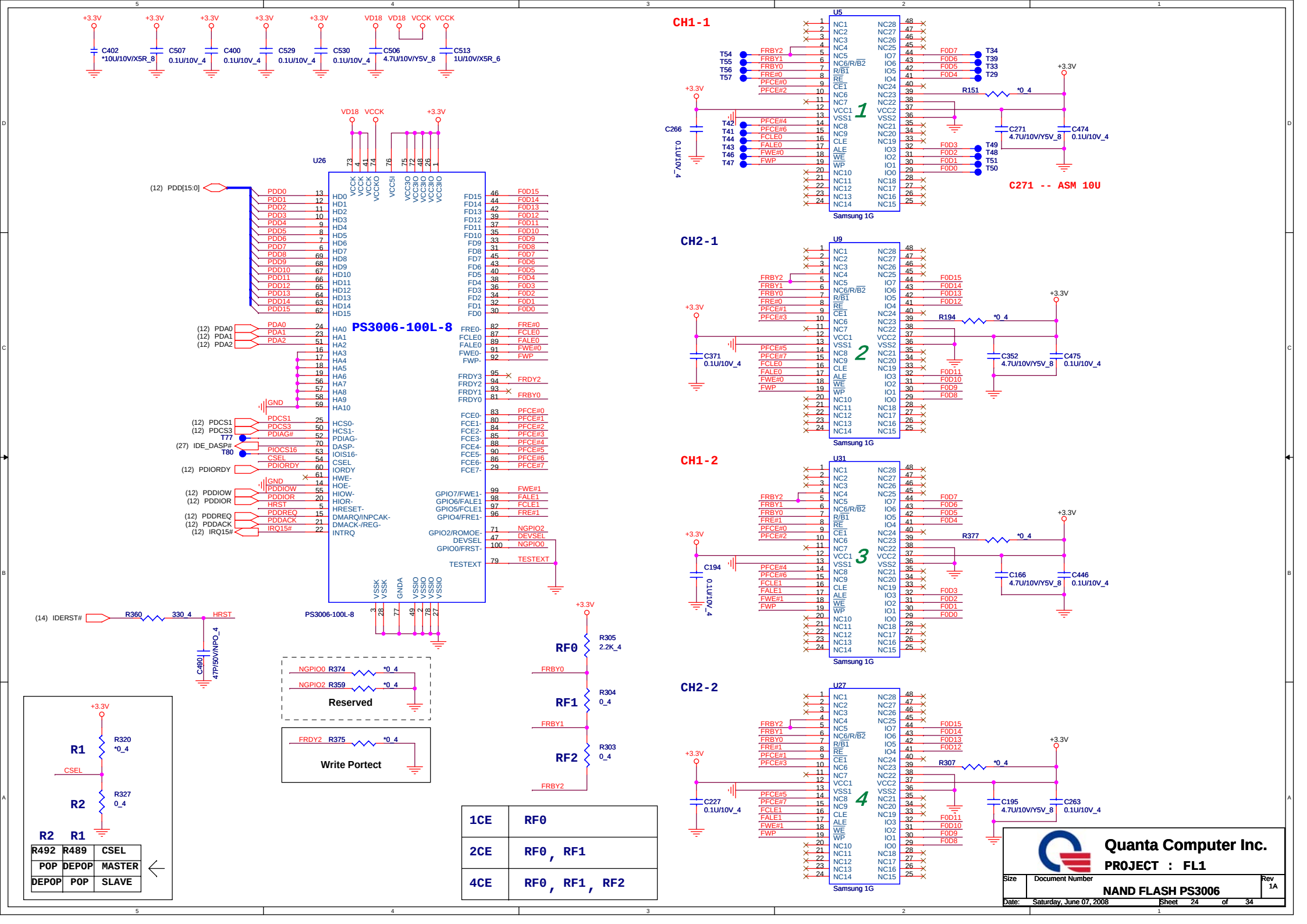


PC BEEP CONTROL

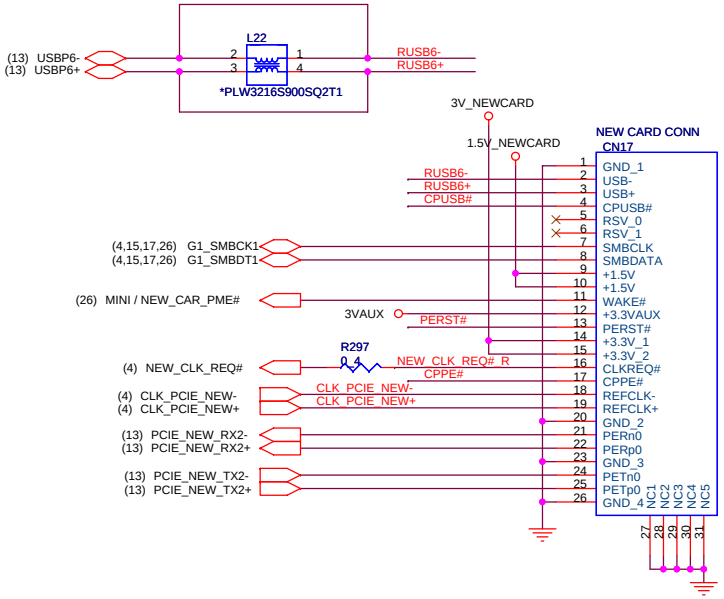


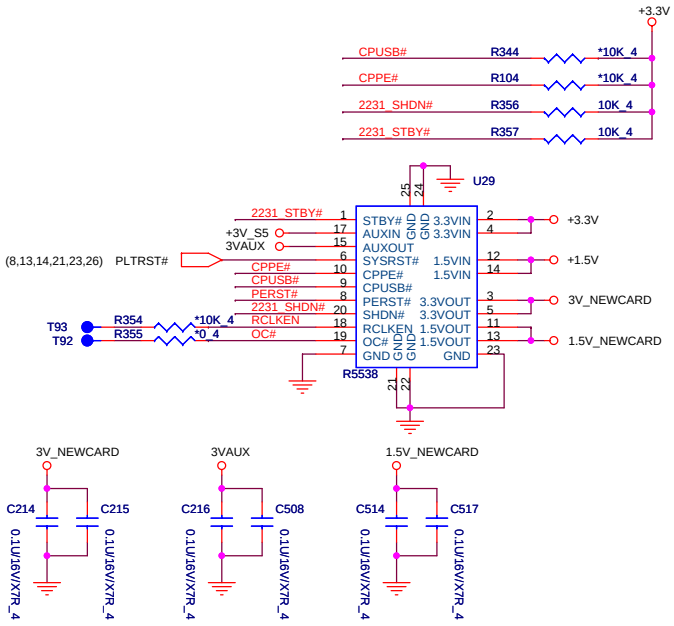
Quanta Computer Inc.			
PROJECT : FL1			
Size	Document Number		Rev
	AUDIO_CODEC ALC269		1A
Date: Saturday, June 07, 2008	Sheet 22 of 34		


BCM5906M
10mm X 10mm
68-Pin QFN
BCM5906MKMLG
Placed near PHY chip
Broadcom recommended cost down solution
need to change P/N
Quanta Computer Inc.
PROJECT : FL1
LAN_BCM5906M / RJ45
Rev 1A
Date: Saturday, June 07, 2008
Sheet 23 of 34

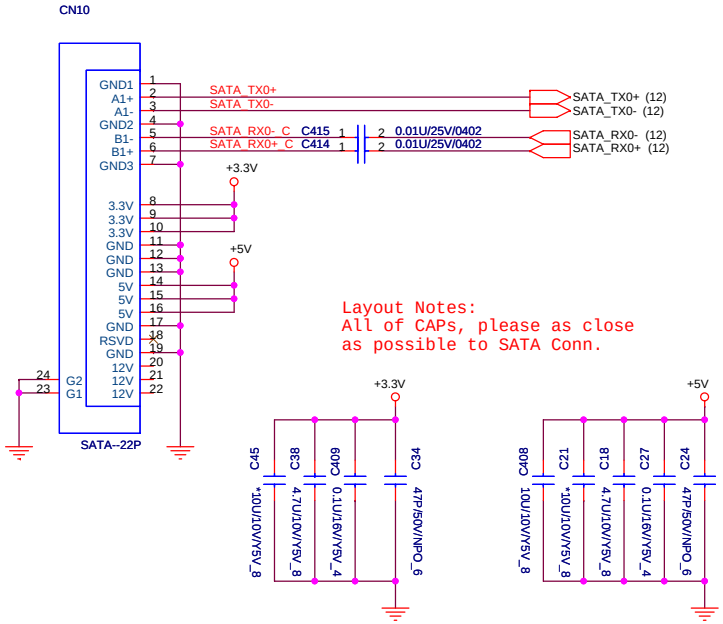

C402 *10U/10V/X5R_8
C507 0.1U/10V_4
C400 0.1U/10V_4
C529 0.1U/10V_4
C530 0.1U/10V_4
C506 4.7U/10V/Y5V_8
C513 1U/10V/X5R_6
U26
PS3006-100L-8
(12) PDD[15:0]
(12) PDA0
(12) PDA1
(12) PDA2
(12) PDCS1
(12) PDCS3
PDIAG#
(27) IDE_DASP#
PIOCS16
CSEL
(12) PDIORDY
(12) PDDIOW
(12) PDDIOR
HRST
(12) PDDREQ
(12) PDDACK
(12) IRQ15#
(14) IDERST#
R360 330_4
C490 47P/50V/NPO_4
NGPIO0 R374 *0_4
NGPIO2 R359 *0_4
Reserved
FRDY2 R375 *0_4
Write Portect
RF0 R305 2.2K_4
RF1 R304 0_4
RF2 R303 0_4
FRBY0
FRBY1
FRBY2
R1 R320 *0_4
R2 0_4
CSEL
R2 R1
R492 R489 CSEL
POP DEPOP MASTER
DEPOP POP SLAVE
1CE RF0
2CE RF0, RF1
4CE RF0, RF1, RF2
CH1-1
CH2-1
CH1-2
CH2-2
U5
U9
U31
U27
Samsung 1G
C266 0.1U/10V_4
C271 4.7U/10V/Y5V_8
C474 0.1U/10V_4
R151 *0_4
C271 -- ASM 10U
C371 0.1U/10V_4
C352 4.7U/10V/Y5V_8
C475 0.1U/10V_4
R194 *0_4
C194 0.1U/10V_4
C166 4.7U/10V/Y5V_8
C446 0.1U/10V_4
R377 *0_4
C227 0.1U/10V_4
C195 4.7U/10V/Y5V_8
C263 0.1U/10V_4
R307 *0_4
Quanta Computer Inc.
PROJECT : FL1
Size
Document Number
NAND FLASH PS3006
Rev 1A
Date: Saturday, June 07, 2008
Sheet 24 of 34

NEW CARD





SATA CONNECTOR



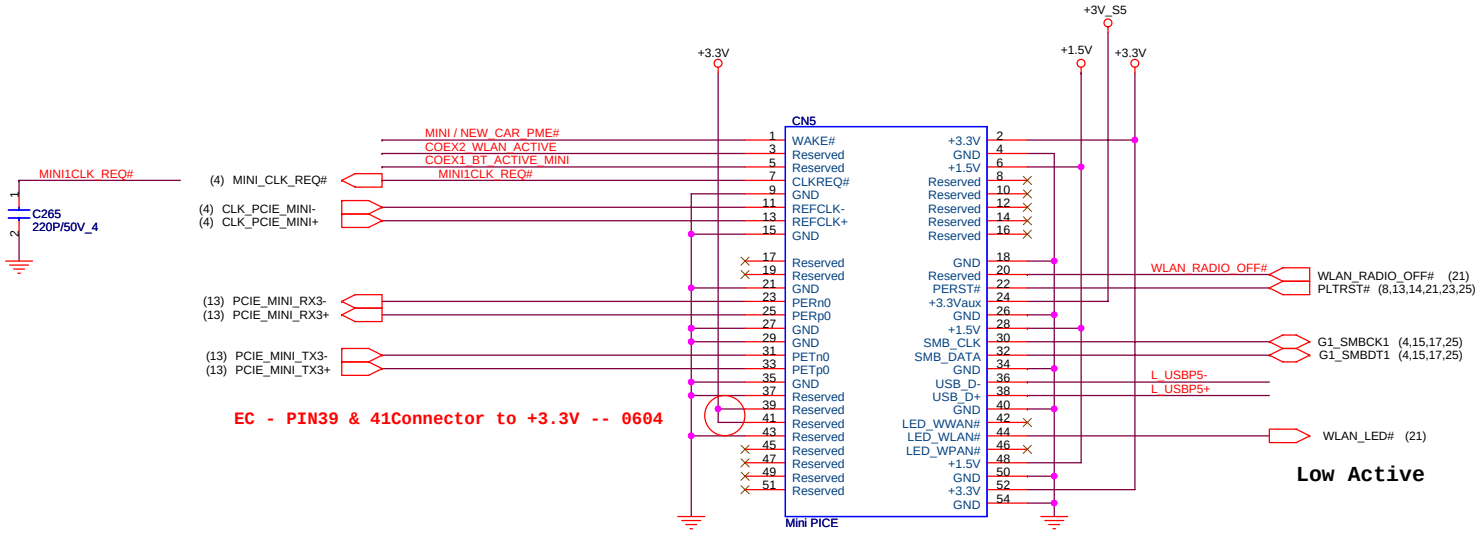
Quanta Computer Inc.

PROJECT : FL1

Size	Document Number	Rev
	NEW CARD / STAT	1A

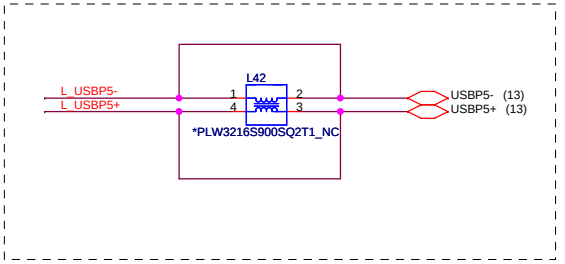
Date: Saturday, June 07, 2008 Sheet 25 of 34

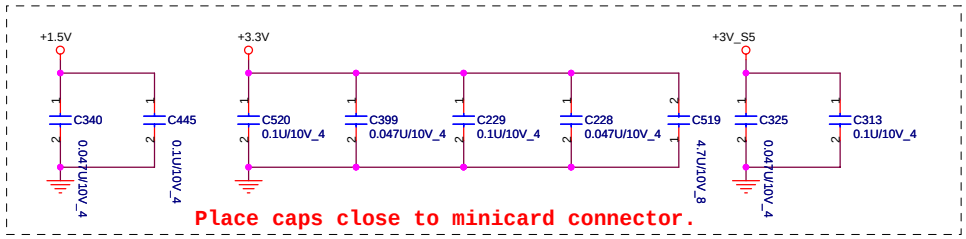
MiniCard WLAN connector



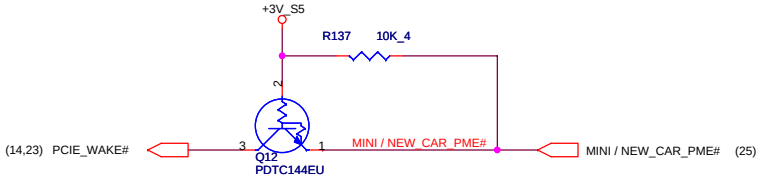
EC - PIN39 & 41Connector to +3.3V -- 0604

Low Active

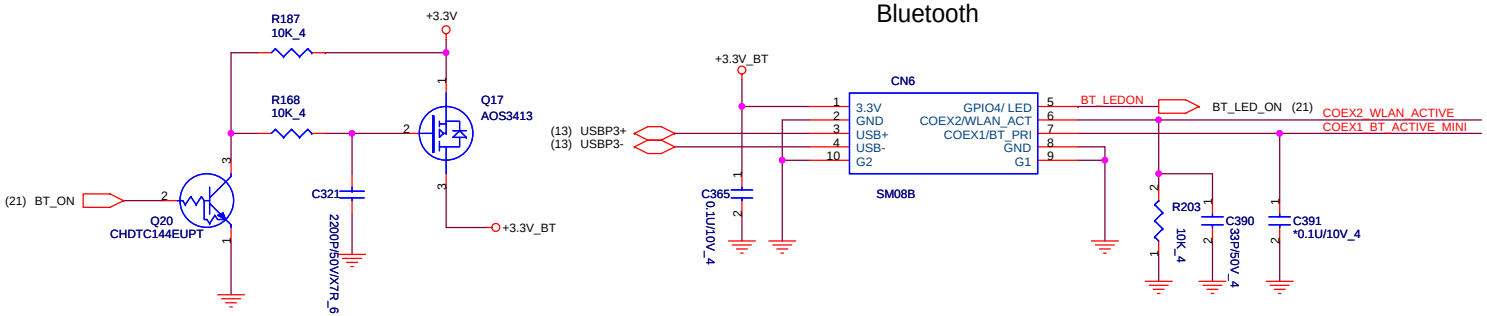




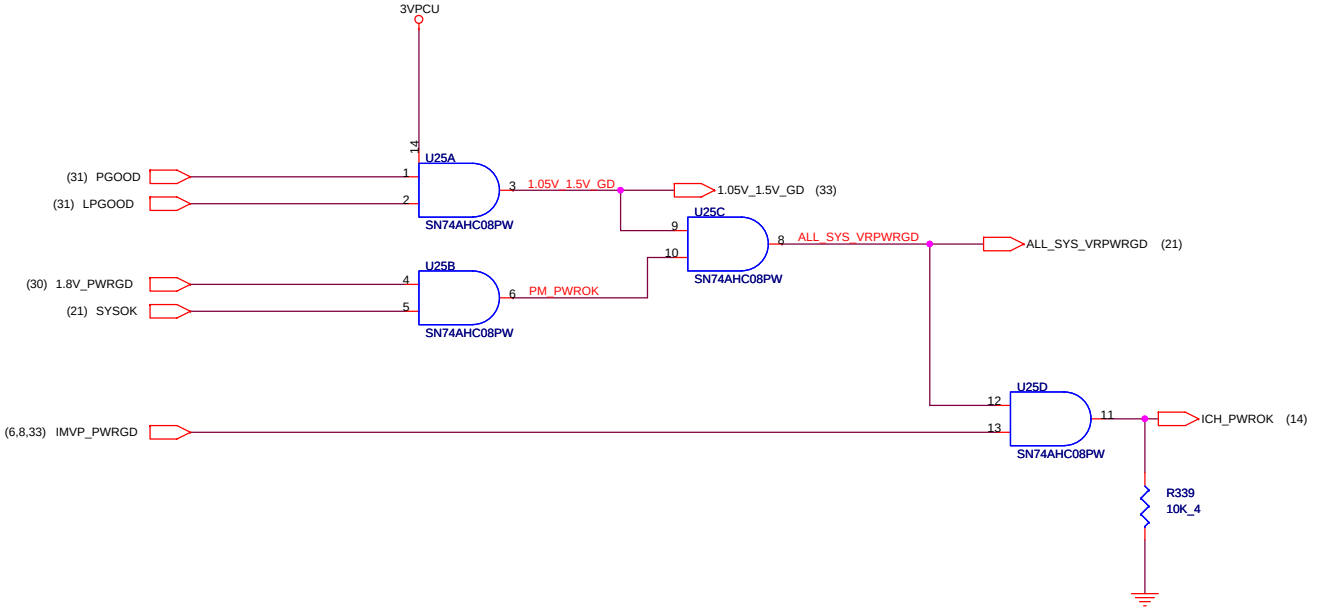
Place caps close to minicard connector.



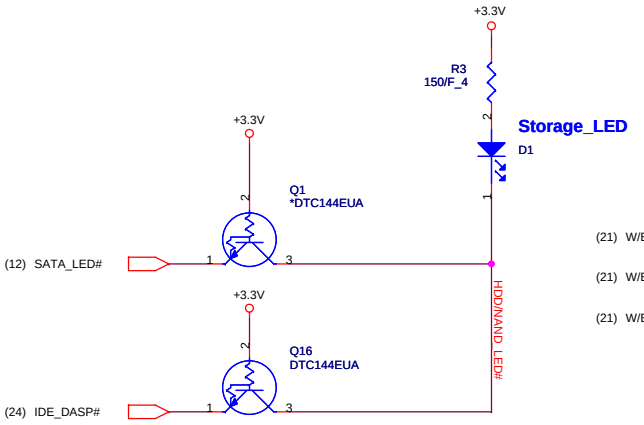
Bluetooth



Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number: MINI CARD (WLAN) / BT	Rev: 1A
Date: Saturday, June 07, 2008	Sheet 26 of 34	

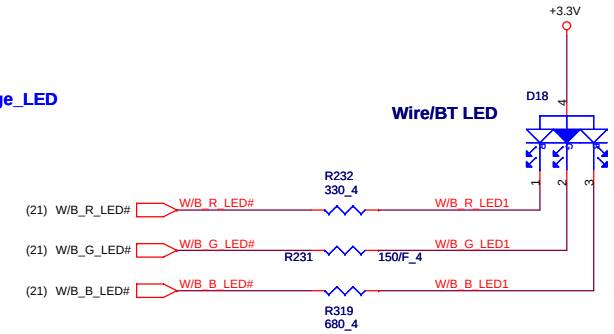


SATA/PATA LED

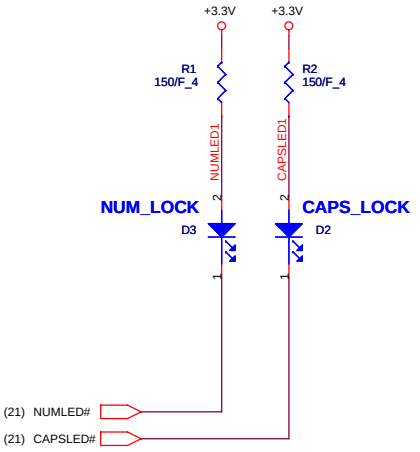


	Q1 & R5	Q16 & R6
SATA	ASM	No-ASM
PATA	No-ASM	ASM

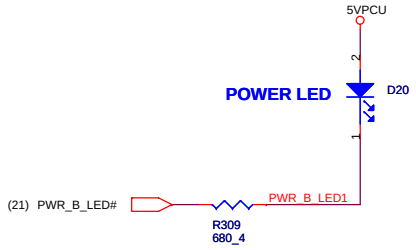
WLAN & BlueTooth LED



Number & CAPS Lock LED



Power Button LED

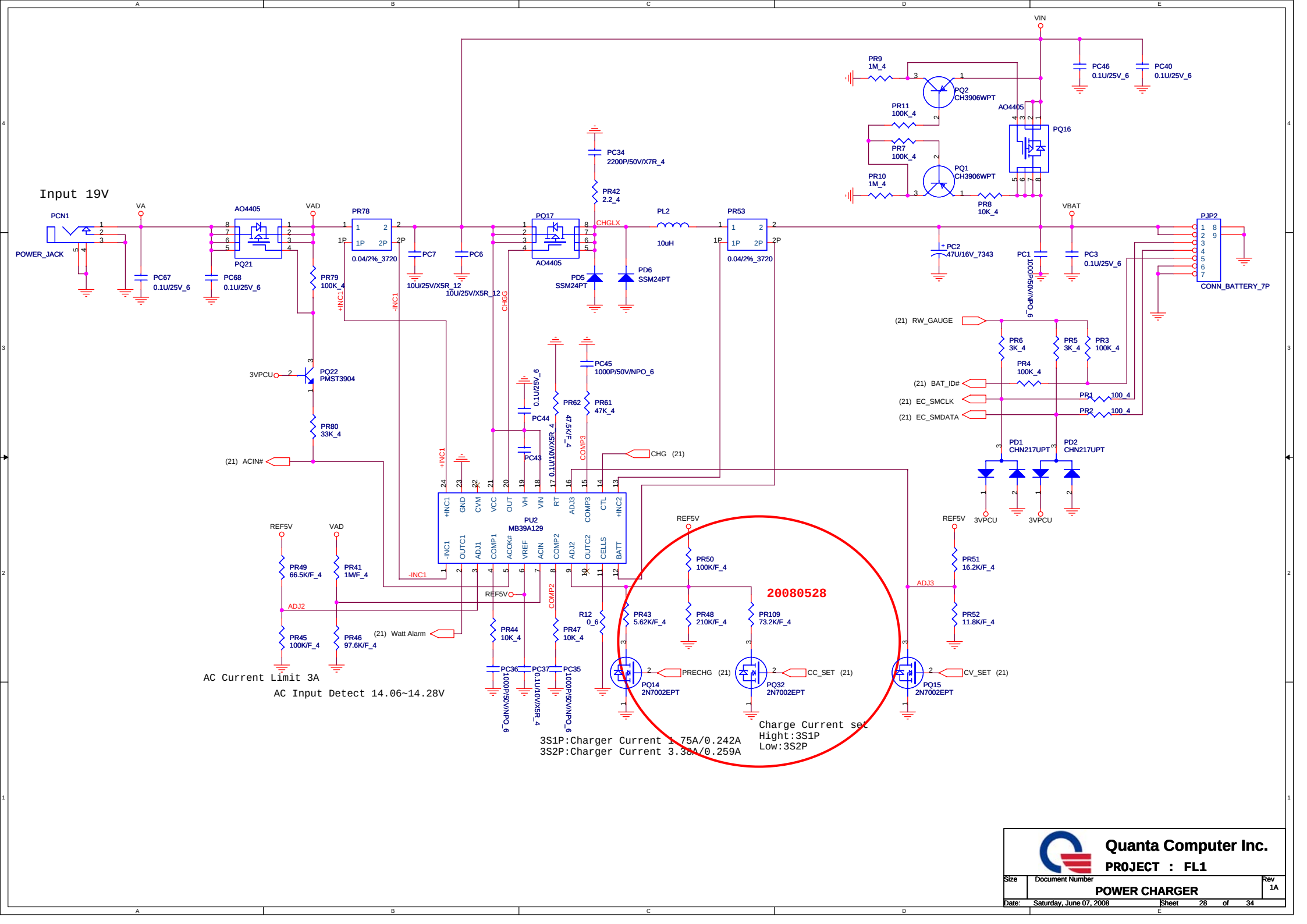


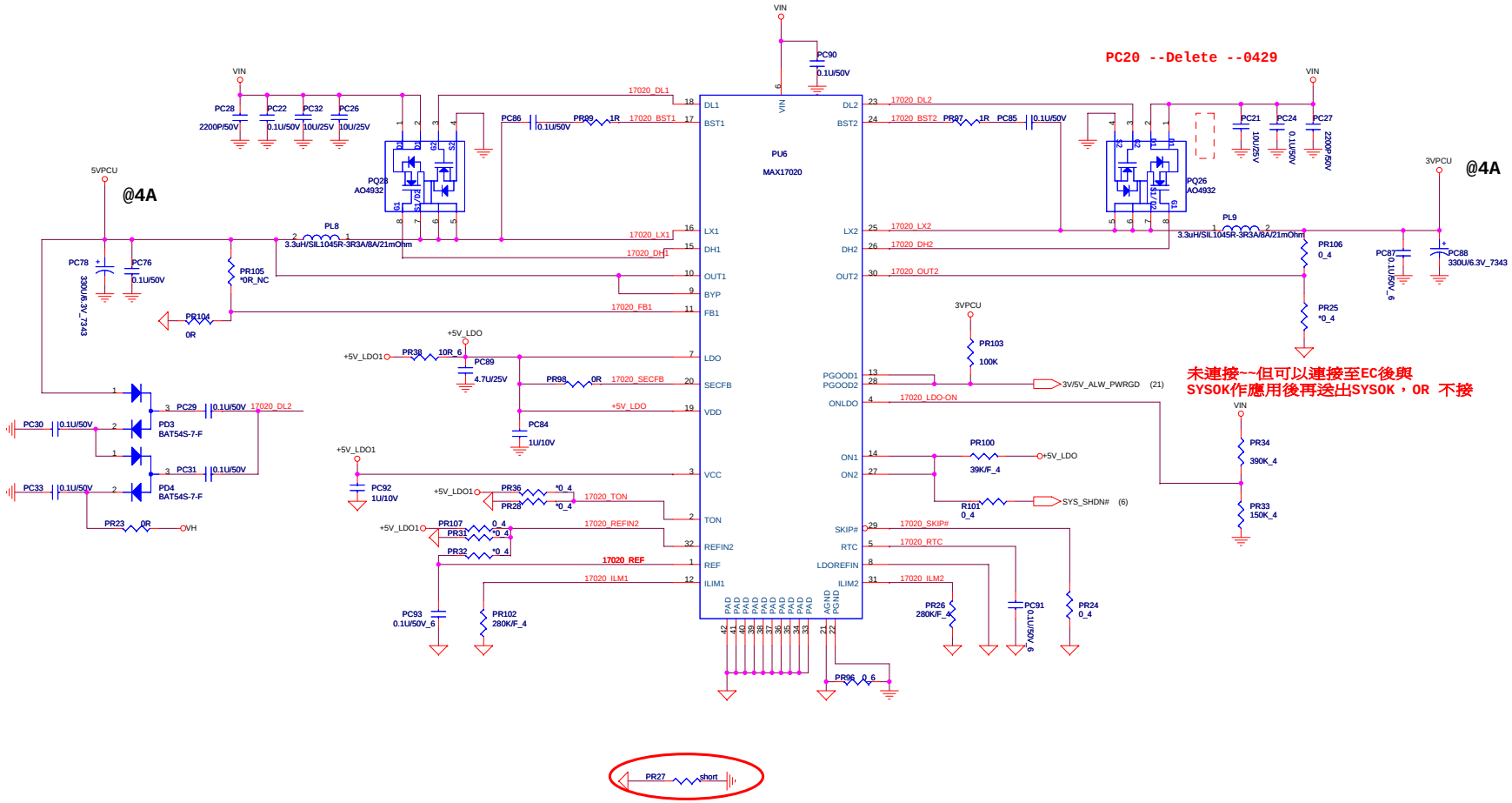
Quanta Computer Inc.

PROJECT : FL1

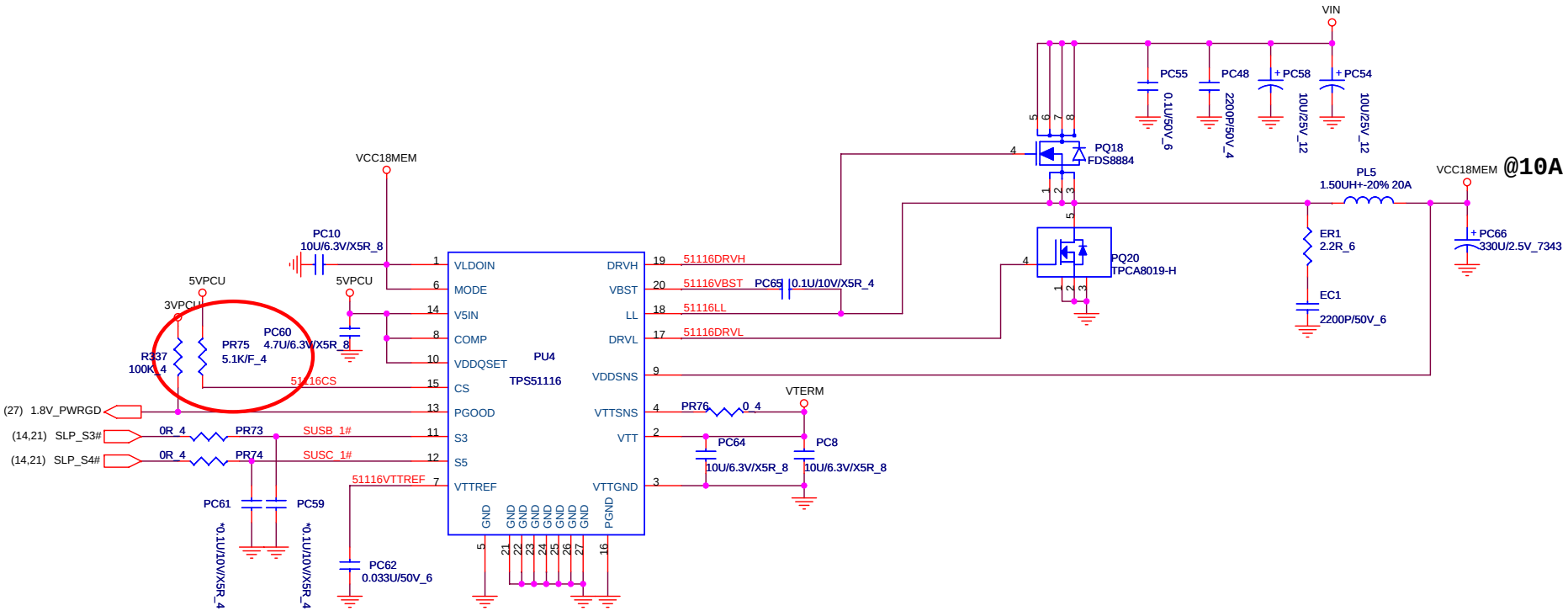
Size	Document Number	Rev
	SYSTEM RESET CIRCUIT / LED	1A

Date: Saturday, June 07, 2008 Sheet 27 of 34


Input 19V
PCN1
POWER_JACK
VA
PC67 0.1U/25V_6
PC68 0.1U/25V_6
AO4405
PQ21
VAD
PR79 100K_4
PR78
0.04/2%_3720
PC7
10U/25V/X5R_12
PC6
10U/25V/X5R_12
PQ17
AO4405
CHGLX
PC34 2200P/50V/X7R_4
PR42 2.2_4
PD5 SSM24PT
PD6 SSM24PT
PL2
10uH
PR53
0.04/2%_3720
VIN
PR9 1M_4
PQ2 CH3906WPT
PR11 100K_4
PR7 100K_4
PR10 1M_4
PQ1 CH3906WPT
AO4405
PQ16
PR8 10K_4
PC46 0.1U/25V_6
PC40 0.1U/25V_6
VBAT
PC2 47U/16V_7343
PC1 1000P/50V/X7R_6
PC3 0.1U/25V_6
PJP2
CONN_BATTERY_7P
(21) RW_GAUGE
PR6 3K_4
PR5 3K_4
PR3 100K_4
PR4 100K_4
(21) BAT_ID#
(21) EC_SMCLK
(21) EC_SMDATA
PR1 100_4
PR2 100_4
PD1 CHN217UPT
PD2 CHN217UPT
3VPCU
3VPCU
3VPCU
PQ22 PMST3904
PR80 33K_4
(21) ACIN#
PC45 1000P/50V/NPO_6
PC44 0.1U/25V_6
PC43 0.1U/10V/X5R_4
PR62 47.5K/F_4
PR61 47K_4
CHG (21)
PU2 MB39A129
+INC1 GND CVM VCC OUT VH VIN RT ADJ3 COMP3 CTL +INC2
-INC1 OUTC1 ADJ1 COMP1 ACOK# VREF ACIN COMP2 ADJ2 OUTC2 CELLS BATT
REF5V
VAD
PR49 66.5K/F_4
PR41 1M/F_4
ADJ2
PR45 100K/F_4
PR46 97.6K/F_4
(21) Watt Alarm
PR44 10K_4
PR47 10K_4
R12 0_6
PC36 1000P/50V/NPO_6
PC37 0.1U/10V/X5R_4
PC35 1000P/50V/NPO_6
REF5V
PR50 100K/F_4
PR43 5.62K/F_4
PR48 210K/F_4
PR109 73.2K/F_4
PQ14 2N7002EPT
PRECHG (21)
PQ32 2N7002EPT
CC_SET (21)
20080528
REF5V
PR51 16.2K/F_4
PR52 11.8K/F_4
PQ15 2N7002EPT
CV_SET (21)
ADJ3
AC Current Limit 3A
AC Input Detect 14.06~14.28V
3S1P:Charger Current 1.75A/0.242A
3S2P:Charger Current 3.38A/0.259A
Charge Current set
Hight:3S1P
Low:3S2P
Quanta Computer Inc.
PROJECT : FL1
Size
Document Number
POWER CHARGER
Rev 1A
Date: Saturday, June 07, 2008
Sheet 28 of 34



Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number: D/D 5V/3V (MAX17020)	Rev: 1A
Date: Saturday, June 07, 2008	Sheet 29 of 34	



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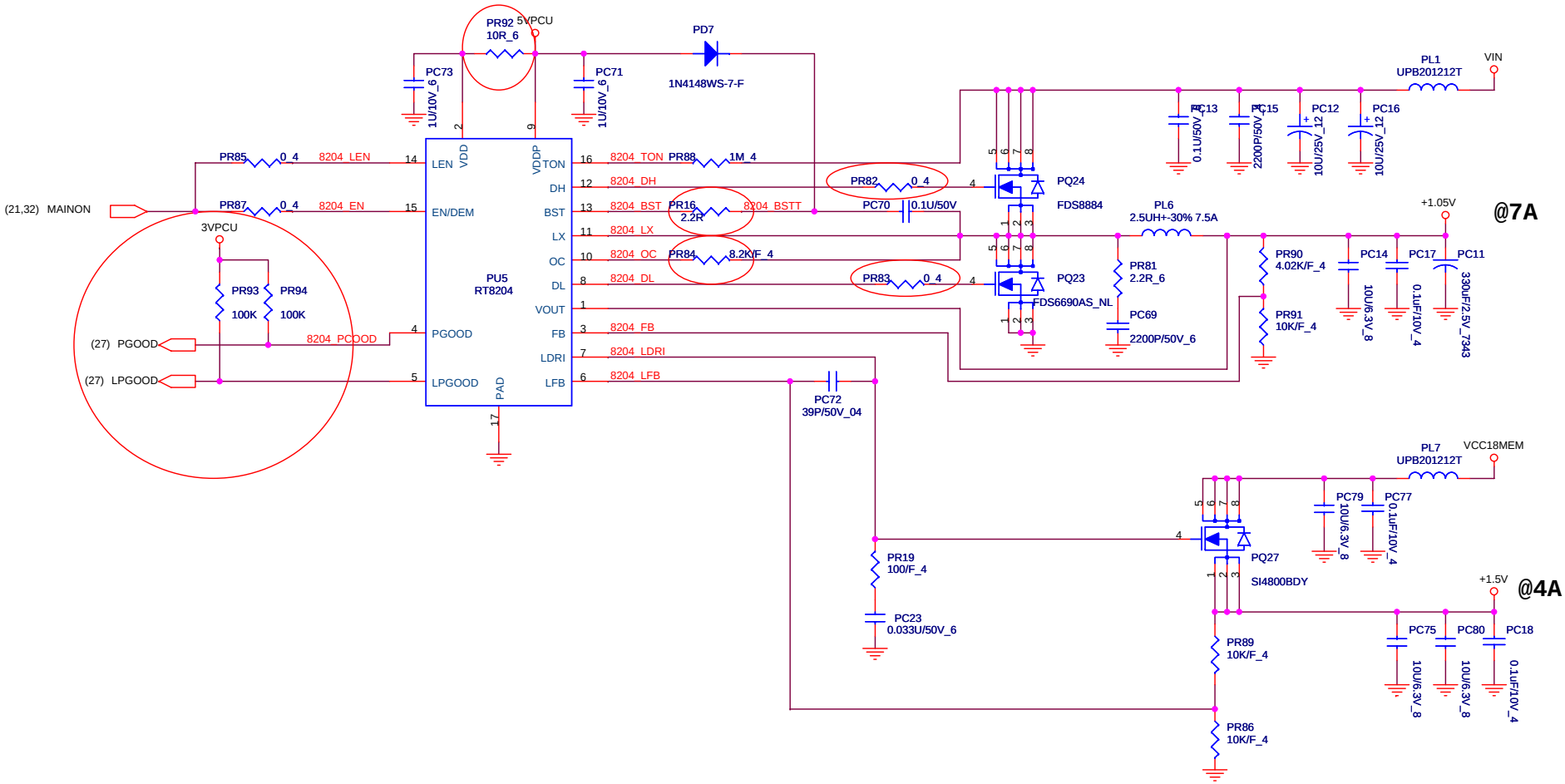
Size	Document Number: DC/DC DDRII / VTERM	Rev: 1A
Date: Saturday, June 07, 2008	Sheet 30 of 34	



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PROJECT : FL1

Size	Document Number	Rev
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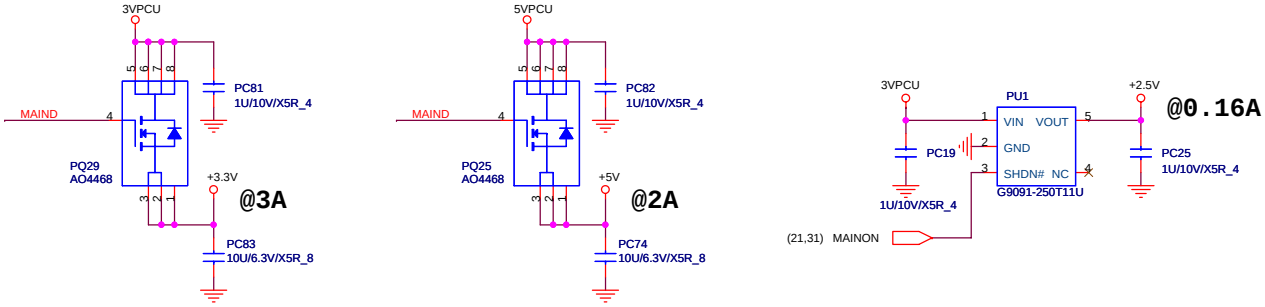
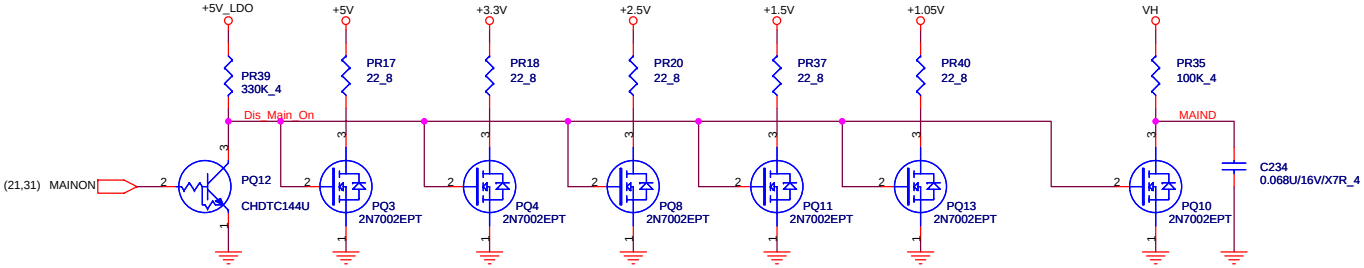
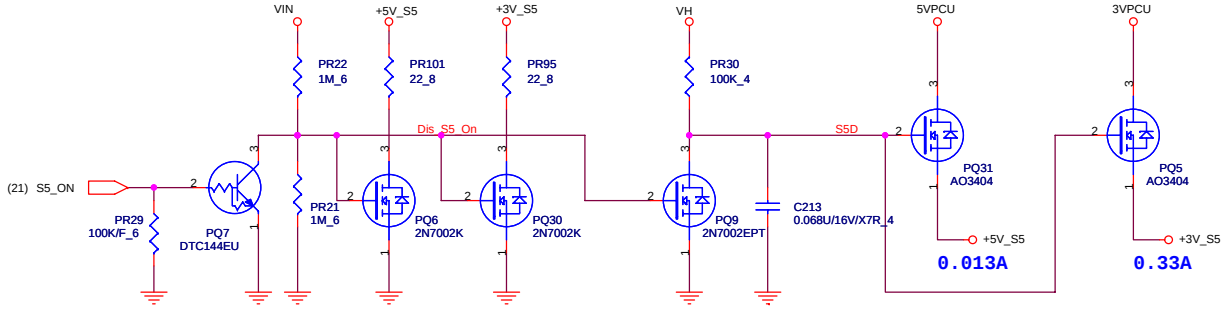
Date: Saturday, June 07, 2008	Sheet 31 of 34	



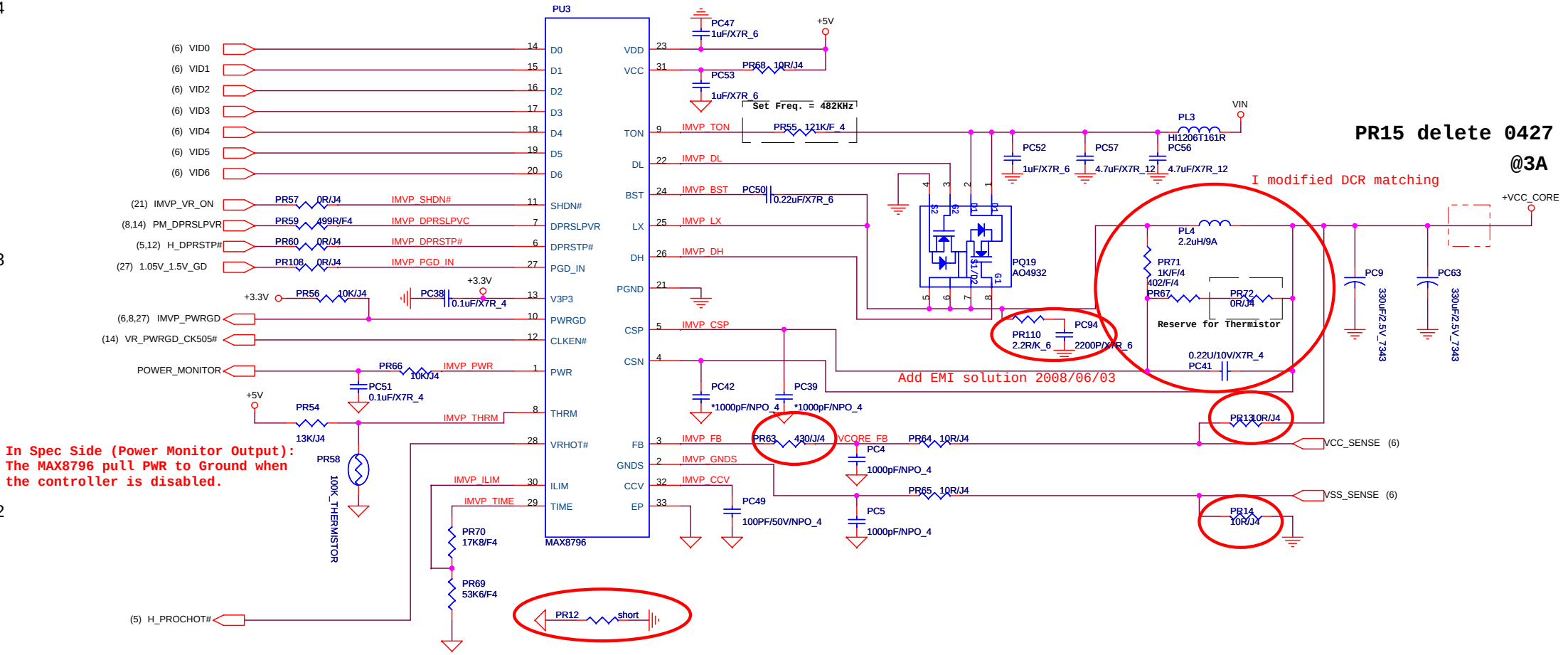
Discharge Circuit Controller by MAINON



Discharge Circuit Controller by S5_ON



Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number: DC/DC Power Rail	Rev: 1A
Date: Saturday, June 07, 2008	Sheet 32 of 34	



Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number	Rev
	CPU CORE POWER	1A
Date: Saturday, June 07, 2008	Sheet 33 of 34	

SCREW HOLE

正面沒有接地銅箔,背面有

HOLE15 *h-c189d98p2

HOLE9 *h-o252x217d115x83sp2

HOLE13 *h-tc217bc236d98p2

HOLE16 *h-c189d98p2

HOLE2 *h-o252x217d115x83p2

HOLE4 *h-c236d98p2

HOLE14 *h-c189d98p2

HOLE7 *h-c189d98p2

(d: 鑽孔的孔徑)背面有露銅

HOLE3 *D218S357HOLD

HOLE5 *h-c354d189pb

HOLE8 *h-c189d189n

HOLE10 *h-c189d189n

108HOLD

HOLE1 *h-c189d83p2

HDD -- HOLD

HOLE17 *h-tcshapebc91d91p2

HOLE18 *h-tcshapebc91d91p2

240HOLD

HOLE12 *h-c236d126pt

HOLE11 *h-c236d126pt

HOLE6 *h-c197d98p2

Quanta Computer Inc.		
PROJECT : FL1		
Size	Document Number: SCREW HOLD	Rev: 1A
Date: Saturday, June 07, 2008	Sheet 34 of 34	

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